



IAC-RK3588-CM

Hardware Manual

Ver. No.: 1.0
2025.4

Company Profile

Zhejiang Qiyang Intelligent Technology Co., Ltd., established in 2007, which locates in Hangzhou, Zhejiang, PRC. It is a high-end technological enterprise that specializes in exploitation, fabrication, and selling embedded computer mainboards. With 10 years of experiences, Qiyang has established the completed service chain from the design concept to mass production successfully.

The R&D team is organized by 30 more technical engineers. Qiyang focus on providing functional embedded hardware, software tool and customization solutions. It has been applied to Industrial Control, Internet of Things, New Retail, Smart Medical, Electricity Device, Environmental Surveillance, Charging Pile etc.

With the growth of the business, Qiyang has set up an SMT factory in Zhuji, Zhejiang province, which is 5000 m², with a 2xSMT production line. The SMT factory performs the ISO9001 Quality Management System strictly. Relying on the solid production ability, the SMT factory's annual capacity is about a million sets, which totally guarantee the delivery date.

Qiyang has a thorough sales marketing network, professional sales, and after-sales team to provide full technical support and service. The business has spread over 120 countries and areas, it helps the clients to introduce the products into the market efficiently and successfully. The combination and extension of research and development, production capacity, and market, that provide a solid foundation for Qiyang to provide specialized, globalized embedded hardware and software.

We offer:

I. Software/Hardware Mainboard

Based on the CPU solution from NXP, Rockchip, MTK, Renesas, TI, Atmel, Cirrus Logic etc, Qiyang provides the ARM development kit/system on module/industrial board and periphery products, paired tools and software for the user do further exploitation.

2. Customization Service

Fully taking the advantage of the technical accumulation on the ARM platform and Linux, Android, Ubuntu OS, Qiyang provides the efficient OEM/ODM service to the users.

Sincerely thanks for using Qiyang's product, we will try our best to offer you the technical supports!

IAC-RK3588-CM Board Use Suggestion

1. Please read the instructions before using the IAC-RK3588-CM core board;
2. Before using, please check the packing list and see whether there is a missing file in the folder;

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3. Please understand the basic structure and composition of IAC-RK3588-CM SOM, including the hardware resource allocation, definition of each pin of the core board and the carrier board, expansion pin definition, etc.;
 4. We accept batch orders for IAC-RK3588-CM SOM module.

Version Record

Version No.	Hardware Platform	Date	Description	Revised by
V1.0	IAC-RK3588-CM V1.00	2024-11	Initial version	Maoh

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NOTE: This manual introduces the hardware interface of the IAC-RK3588-CM Core Board

I. System Composition

1.1. Chipset Overview

IAC-RK3588-CM adopts Rockchip RK3588 processor which is a low power, high performance processor for ARM-based PC and Edge Computing device, personal mobile internet device and other digital multimedia applications, and integrates quad-core Cortex-A76 and quad-core Cortex-A55 with separately NEON coprocessor.

Many embedded powerful hardware engines provide optimized performance for high-end application. RK3588 supports H.265 and VP decoder by 8K@60fps, H.264 decoder by 8K@30fps, and AV1 decoder by 4K@60fps, also supports H.264 and H.265 encoder by 8K@30fps, high-quality JPEG encoder/decoder, specialized image preprocessor and postprocessor.

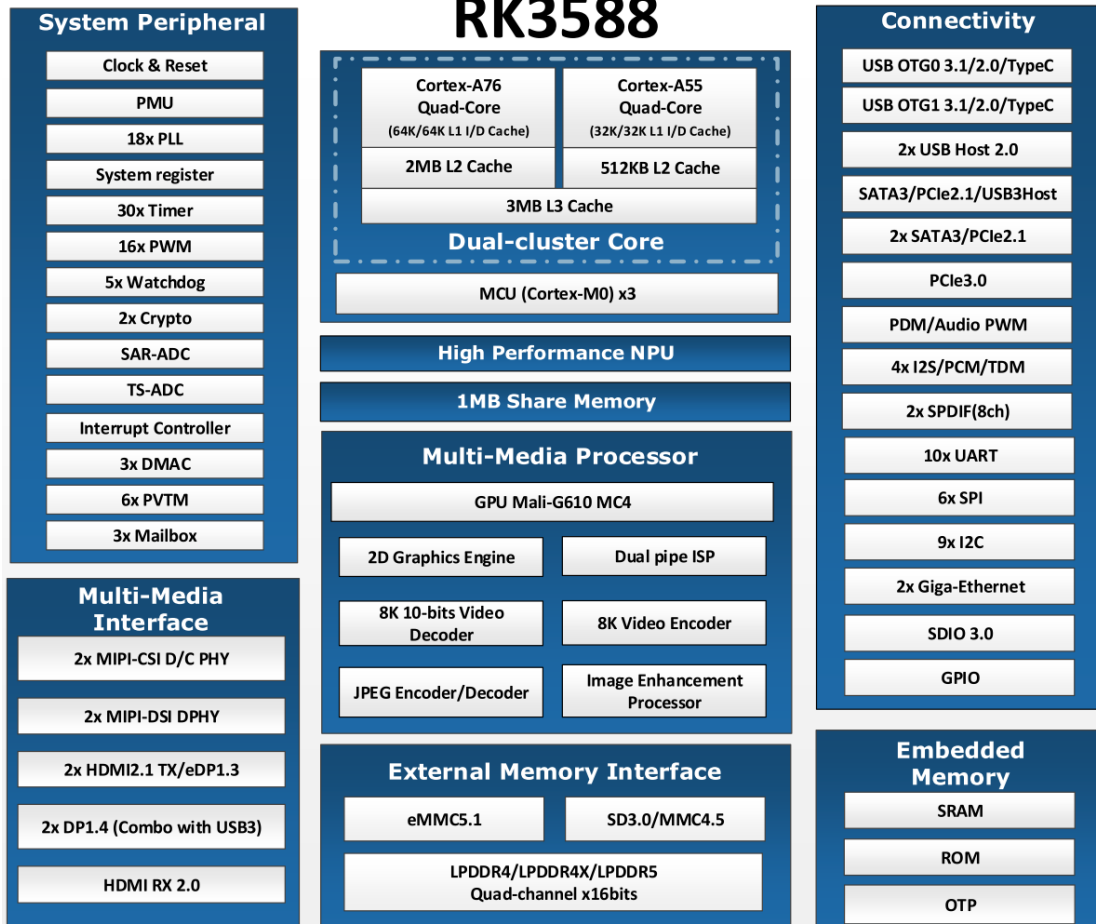
Embedded 3D GPU makes RK3588 completely compatible with OpenGL ES 1.1, 2.0, and 3.2, OpenCL up to 2.2 and Vulkan 1.2. Special 2D hardware engine with MMU will maximize display performance and provide very smooth operation.

RK3588 introduces a new generation totally hardware-based maximum 48-Megapixel ISP (image signal processor). It implements a lot of algorithm accelerators, such as HDR, 3A, LSC, 3DNR, 2DNR, sharpening, dehaze, fisheye correction, gamma correction and so on.

The built-in NPU supports INT4/INT8/INT16/FP16 hybrid operation and computing power is up to 6TOPs. In addition, with its strong compatibility, network models based on a series of frameworks such as TensorFlow/MXNet/PyTorch/Caffe can be easily converted.

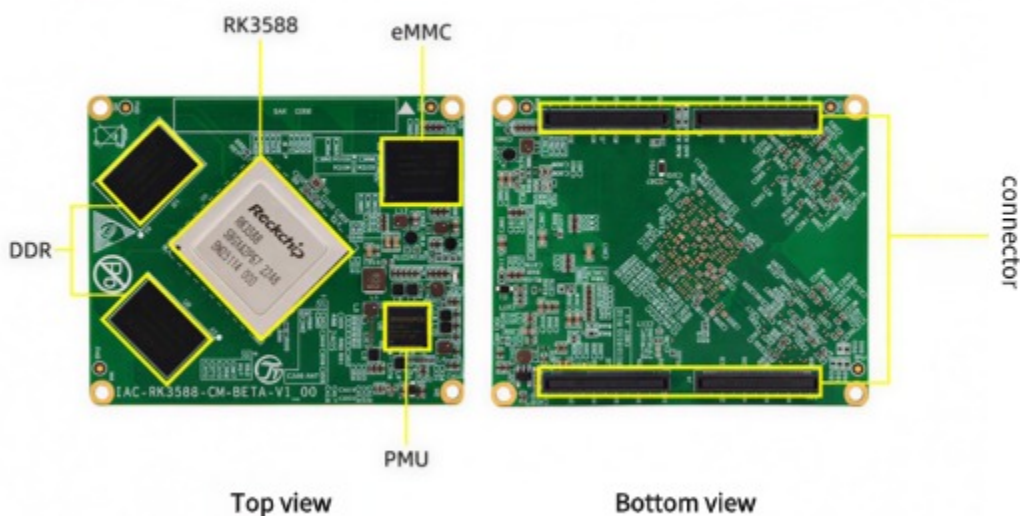
RK3588 has high-performance quad channel external memory interface (LPDDR4/LPDDR4X/LPDDR5) capable of sustaining demanding memory bandwidths, also provides a complete set of peripheral interface to support very flexible applications

RK3588



1.2.Core Board Resource Overview

IAC-RK3588-CM SOM module adopts 10-layer PCB high precision immersion gold process, high TG board, with reliable electricity and anti-interference performance. Integrated with CPU,LPDDR4,eMMC, PMU, etc. It adopts board to board receptacle which has derived 400 pins from the SOM module. It fully extended RK3588 processor resources, and it is available to design a tailored carrier board based on the multiplexed pins.



- ◆ Onboard with RockChip RK3588 processor;
- ◆ Onboard with 8GB LPDDR4, 64GB eMMC (Std. option);
- ◆ The SOM adopts 10-layer PCB high precision immersion gold technology;
- ◆ SOM size: 68mm*55mm, it suits for various application scenarios;
- ◆ The SOM has 4 pieces of board to board connector to derive SOM resources;
- ◆ +4V DC, onboard with PMU;
- ◆ Debian11.
- ◆ Android13

1.3. SOM Resource

Hardware Resources	CPU	RockChip RK3588 processor
	Processor	Quad ARM® Cortex™-A76 core, Quad ARM® Cortex™-A55 core RK3588 main frequency up to 2.2GHz 3* ARM® Cortex™-M0
	GPU	ARM Mail-G610 MP4 OpenGL ES 1.1 2.0 3.2、Vulkan1.2、Open CL 2.2
	VPU	Support 8K 30fps H.264 video decoding Support 8K 60fps H.265/VP9/AVS2 video decoding Support 4K 60fps AV1 video decoding Support 1080P 60fps MPEG-2/MPEG-1/VC-1/VP8 video decoding Support 8K 30fps H.265/H.264 video encoding
	NPU	Neural Processor Unit: Supports 6TOPS
	RAM	8GB LPDDR4,16GB is optional
	Flash	64GB eMMC,128GB is optional
	PMIC	RockChip RK806 PMU
	Network	2xRGMII, 10M/100M/1000Mbps is supportive.
	Connectivity	7xUART (Including 1xDebug UART)
		2xPCIE3.0X2, 1xPCIE2.0X3
	Display	2x4-lane MIPI-DSI interface, it reaches up to 4K@60fps
		2xHDMI Tx display interface, the resolution is up to 7680x4320p@60fps (1-ch MUX eDP)
		1xeDP interface, the resolution reaches up to 4K@60fps (MUX HDMI TX)
	Audio	1x I2S
	USB	2x full functional USB 3.0 Type-C OTG& DP
		2x USB 2.0 HOST
	Camera	2x MIPI-CSI (4-Lane), the resolution supports 2*2K@120Hz
	Input	5x I2C
3x 8bit ADC		
1xHDMI RX , the resolution reaches up to 2160@60Hz		
Expansion	19xGPIO	
	1x32K CLK Out.	
	4xPWM	
Storage Socket	1xSDIO	
Power Input	+4V DC	
Electrical Character	Layer/Size	SOM Size:68mm*55mm, 10-layer high precision immersion gold technology
	Power Consumptio	Whole board's consumption <3W (Non-load)

n	
Operation Temp.	0°C ~ +60°C
Storage Temp.	-10°C ~ +85°C
Humidity	5%-90%, non-condensation
SOM options	Standard option: 8GB+64GB,consumer grade (0°C ~ +60°C)

1.4.Pin Definition

J1: Pin Definition from SOM

MUX GPIO	Signal Name	PIN#	PIN#	Signal Name	MUX GPIO
	MIPI_CSI0_RX_CLK1P	1	2	MIPI_CSI1_RX_CLK1N	
	MIPI_CSI0_RX_CLK1N	3	4	MIPI_CSI1_RX_CLK1P	
	GND	5	6	GND	
	MIPI_CSI0_RX_D3P	7	8	MIPI_CSI1_RX_D3N	
	MIPI_CSI0_RX_D3N	9	10	MIPI_CSI1_RX_D3P	
	GND	11	12	GND	
	MIPI_CSI0_RX_D2P	13	14	MIPI_CSI1_RX_D2N	
	MIPI_CSI0_RX_D2N	15	16	MIPI_CSI1_RX_D2P	
	GND	17	18	GND	
	MIPI_CSI0_RX_CLK0P	19	20	MIPI_CSI1_RX_CLK0N	
	MIPI_CSI0_RX_CLK0N	21	22	MIPI_CSI1_RX_CLK0P	
	GND	23	24	GND	
	MIPI_CSI0_RX_D1P	25	26	MIPI_CSI1_RX_D1N	
	MIPI_CSI0_RX_D1N	27	28	MIPI_CSI1_RX_D1P	
	GND	29	30	GND	
	MIPI_CSI0_RX_D0P	31	32	MIPI_CSI1_RX_D0N	
	MIPI_CSI0_RX_D0N	33	34	MIPI_CSI1_RX_D0P	
	GND	35	36	GND	
	GMAC1_RXCLK	37	38	PCIE20X1_2_CLKREQ_M0	

	GMAC1_RXDV_CRS	39	40	PCIE20X1_2_WAKEN_M0	
	GMAC1_RXD0	41	42	PCIE20X1_2_PERSTN_M0	
	GMAC1_RXD1	43	44	UART9_CTSN_M2	
	GMAC1_RXD2	45	46	UART9_RTSN_M2	
	GMAC1_RXD3	47	48	UART9_RX_M2	
	GMAC1_TXCLK	49	50	UART9_TX_M2	
	GMAC1_TXEN	51	52	UART5_RX_M1	
	GMAC1_TXD0	53	54	UART5_TX_M1	
	GMAC1_TXD1	55	56	GPIO3_C6_u	
	GMAC1_TXD2	57	58	GPIO3_B2_d	
	GMAC1_TXD3	59	60	GND	
	GND	61	62	ETH1_REFCLKO_25M	
	GMAC1_MDC	63	64	GMAC1_MCLKINOUT	
	GMAC1_MDIO	65	66	GND	
	GND	67	68	GPIO3_B7_d	
	GMAC0_RXCLK	69	70	UART7_TX_M1	
	GMAC0_RXDV_CRS	71	72	UART7_RX_M1	
	GMAC0_RXD0	73	74	HDMI_TX1_SDA_M0	
	GMAC0_RXD1	75	76	HDMI_TX1_SCL_M0	
	GMAC0_RXD2	77	78	HDMI_TX1_CEC_M0	
	GMAC0_RXD3	79	80	CLK32K_OUT1	
	GMAC0_TXCLK	81	82	GPIO4_C6_d	
	GMAC0_TXEN	83	84	GND	
	GMAC0_TXD0	85	86	ETH0_REFCLKO_25M	
	GMAC0_TXD1	87	88	GMAC0_MCLKINOUT	
	GMAC0_TXD2	89	90	GND	
	GMAC0_TXD3	91	92	GPIO0_A0_d	

	GND	93	94	SDMMC_DET_L	
	GMAC0_MDIO	95	96	GPIO0_B0_Z	
	GMAC0_MDC	97	98	GPIO0_B3_Z	
	GND	99	100	GND	

J2: Pin Definition from SOM

MUX GPIO	Signal Name	PIN#	PIN#	Signal Name	MUX GPIO
	VCC_SYS_4V0	1	2	VCC_SYS_4V0	
	VCC_SYS_4V0	3	4	VCC_SYS_4V0	
	VCC_SYS_4V0	5	6	VCC_SYS_4V0	
	VCC_SYS_4V0	7	8	VCC_SYS_4V0	
	VCC_SYS_4V0	9	10	VCC_SYS_4V0	
	VCC_SYS_4V0	11	12	VCC_SYS_4V0	
	VCC_SYS_4V0	13	14	VCC_SYS_4V0	
	GND	15	16	GND	
	GND	17	18	GND	
	GND	19	20	GND	
	GND	21	22	GND	
	GND	23	24	VCC_PLDO1_1V8_S0	
	SYS_RESETn	25	26	VCC_BUCK10_1V8_S3	
	PMIC_PWRON	27	28	VCC_BUCK8_3V3_S3	
	PMIC_EXT_EN	29	30	VCC_BUCK8_3V3_S3	
	GND	31	32	GND	
	MIPI_DPHY0_RX_D3P	33	34	MIPI_DPHY0_RX_D3P	
	MIPI_DPHY0_RX_D3N	35	36	MIPI_DPHY0_TX_D3N	
	MIPI_DPHY0_RX_D2P	37	38	MIPI_DPHY0_TX_D2P	
	MIPI_DPHY0_RX_D2N	39	40	MIPI_DPHY0_TX_D2N	

	GND	41	42	GND	
	MIPI_DPHY0_RX_CLKP	43	44	MIPI_DPHY0_TX_CLKP	
	MIPI_DPHY0_RX_CLKN	45	46	MIPI_DPHY0_TX_CLKN	
	GND	47	48	GND	
	MIPI_DPHY0_RX_D1P	49	50	MIPI_DPHY0_TX_D1P	
	MIPI_DPHY0_RX_D1N	51	52	MIPI_DPHY0_TX_D1N	
	MIPI_DPHY0_RX_D0P	53	54	MIPI_DPHY0_TX_D0P	
	MIPI_DPHY0_RX_D0N	55	56	MIPI_DPHY0_TX_D0N	
	GND	57	58	GND	
	MIPI_DPHY1_RX_D3P	59	60	MIPI_DPHY1_TX_D3P	
	MIPI_DPHY1_RX_D3N	61	62	MIPI_DPHY1_TX_D3N	
	MIPI_DPHY1_RX_D2P	63	64	MIPI_DPHY1_TX_D2P	
	MIPI_DPHY1_RX_D2N	65	66	MIPI_DPHY1_TX_D2N	
	GND	67	68	GND	
	MIPI_DPHY1_RX_CLKP	69	70	MIPI_DPHY1_TX_CLKP	
	MIPI_DPHY1_RX_CLKN	71	72	MIPI_DPHY1_TX_CLKN	
	GND	73	74	GND	
	MIPI_DPHY1_RX_D1P	75	76	MIPI_DPHY1_TX_D1P	
	MIPI_DPHY1_RX_D1N	77	78	MIPI_DPHY1_TX_D1N	
	MIPI_DPHY1_RX_D0P	79	80	MIPI_DPHY1_TX_D0P	
	MIPI_DPHY1_RX_D0N	81	82	MIPI_DPHY1_TX_D0N	
	GND	83	84	GND	
	UART8_RTSN_M0	85	86	UART8_TX_M0	
	UART8_CTSN_M0	87	88	UART8_RX_M0	
	SARADC_IN2	89	90	GND	
	SARADC_IN1_RECOVER	91	92	TYPEC0_SSTX2N/DP0_TX3	
	Y			N	

	SARADC_IN0_BOOT	93	94	TYPEC0_SSTX2P/DP0_TX3 P	
	GND	95	96	GND	
	TYPEC0_SBU2/DP0_AUX N	97	98	TYPEC0_SSRX2P/DP0_TX 2P	
	TYPEC0_SBU1/DP0_AUX P	99	100	TYPEC0_SSRX2N/DP0_TX 2N	

J3: Pin Definition from SOM

MUX GPIO	Signal Name	PIN#	PIN#	Signal Name	MUX GPIO
	PCIE20_2_RXN	1	2	PCIE30_PORT0_RX0P	
	PCIE20_2_RXP	3	4	PCIE30_PORT0_RX0N	
	GND	5	6	GND	
	PCIE20_2_TXN	7	8	PCIE30_PORT0_RX1P	
	PCIE20_2_TXP	9	10	PCIE30_PORT0_RX1N	
	GND	11	12	PCIE30_TX1N	
	PCIE20_REFCLKN	13	14	PCIE30_PORT0_REFCLKN _IN	
	PCIE20_REFCLKP	15	16	PCIE30_PORT0_REFCLKP _IN	
	GND	17	18	GND	
	PCIE20_0_RXP	19	20	PCIE30_PORT0_TX0N	
	PCIE20_0_RXN	21	22	PCIE30_PORT0_TX0P	
	GND	23	24	GND	
	PCIE20_0_TXN	25	26	PCIE30_PORT0_TX1N	
	PCIE20_0_TXP	27	28	PCIE30_PORT0_TX1P	
	GND	29	30	GND	

	PCIE20_0_REFCLKP	31	32	PCIE30_PORT1_RX0P	
	PCIE20_0_REFCLKN	33	34	PCIE30_PORT1_RX0N	
	GND	35	36	GND	
	PCIE20_1_TXP	37	38	PCIE30_PORT1_RX1P	
	PCIE20_1_TXN	39	40	PCIE30_PORT1_RX1N	
	GND	41	42	GND	
	PCIE20_1_RXP	43	44	PCIE30_PORT1_TX0P	
	PCIE20_1_RXN	45	46	PCIE30_PORT1_TX0N	
	GND	47	48	GND	
	PCIE20_1_REFCLKP	49	50	PCIE30_PORT1_TX1P	
	PCIE20_1_REFCLKN	51	52	PCIE30_PORT1_TX1N	
	GND	53	54	GND	
	I2C6_SCL_M0	55	56	PCIE30_PORT1_REFCLKP _IN	
	I2C6_SDA_M0	57	58	PCIE30_PORT1_REFCLKN _IN	
	PWM1_M0	59	60	GND	
	PWM0_M0	61	62	GPIO0_C6_u	
	PWM2_M0	63	64	GPIO0_D3_u	
	PWM4_M0	65	66	GPIO1_A7_u	
	UART2_RX_M0	67	68	GPIO1_A4_d	
	UART2_TX_M0	69	70	HDMI_TX0_HPD_M0	
	GND	71	72	HDMI_TX1_HPD_M0	
	I2C3_SDA_M0	73	74	PCIE30X4_CLKREQn_M3	
	I2C3_SCL_M0	75	76	PCIE30X4_WAKEn_M3	
	GPIO1_C4_d	77	78	PCIE30X4_PERSTn_M3	
	GPIO1_C6_d	79	80	PCIE30X1_0_CLKREQn_M2	

	GPIO1_D5_d	81	82	PCIE30X1_0_WAKEN_M2	
	I2S0_MCLK	83	84	PCIE30X1_0_PERSTN_M2	
	I2S0_SCLK	85	86	VCC_IO_SD	
	I2S0_LRCK	87	88	GND	
	I2S0_SDO0	89	90	I2C2_SCL_M4	
	I2S0_SDI0	91	92	I2C2_SDA_M4	
	I2S0_SDI1	93	94	HDMI_RX_CEC_M2	
	GPIO1_D2_d	95	96	HDMI_RX_HPDOUT_M2	
	I2C7_SDA_M0	97	98	HDMI_RX_SDA_M2	
	I2C7_SCL_M0	99	100	HDMI_RX_SCL_M2	

J4: Pin Definition from SOM

MUX GPIO	Signal Name	PIN#	PIN#	Signal Name	MUX GPIO
	TYPEC0_OTG_DM	1	2	TYPEC0_SSTX1N/DP0_T X1N	
	TYPEC0_OTG_DP	3	4	TYPEC0_SSTX1P/DP0_T X1P	
	GND	5	6	GND	
	TYPEC0_OTG_ID	7	8	TYPEC0_SSRX1P/DP0_T X0P	
	TYPEC0_OTG_VBUSDET	9	10	TYPEC0_SSRX1N/DP0_T X0N	
	GND	11	12	GND	
	TYPEC1_SBU2/DP1_AUX N	13	14	TYPEC1_SSTX2N/DP1_T X3N	
	TYPEC1_SBU1/DP1_AUX P	15	16	TYPEC1_SSTX2P/DP1_T X3P	

	GND	17	18	GND	
	TYPEC1_OTG_DM	19	20	TYPEC1_SSRX2P/DP1_T X2P	
	TYPEC1_OTG_DP	21	22	TYPEC1_SSRX2N/DP1_T X2N	
	GND	23	24	GND	
	USB2_HOST1_DM	25	26	TYPEC1_SSTX1N/DP1_T X1N	
	USB2_HOST1_DP	27	28	TYPEC1_SSTX1P/DP1_T X1P	
	GND	29	30	GND	
	USB2_HOST0_DM	31	32	TYPEC1_SSRX1P/DP1_T X0P	
	USB2_HOST0_DP	33	34	TYPEC1_SSRX1N/DP1_T X0N	
	GND	35	36	GND	
	HDMI_TX0_D2P/EDP_TX0 _D2P	37	38	HDMI_TX1_D2P/EDP_TX1 _D2P	
	HDMI_TX0_D2N/EDP_TX 0_D2N	39	40	HDMI_TX1_D2N/EDP_TX 1_D2N	
	GND	41	42	GND	
	HDMI_TX0_D1P/EDP_TX0 _D1P	43	44	HDMI_TX1_D1P/EDP_TX1 _D1P	
	HDMI_TX0_D1N/EDP_TX 0_D1N	45	46	HDMI_TX1_D1N/EDP_TX 1_D1N	
	GND	47	48	GND	
	HDMI_TX0_D0P/EDP_TX0 _D0P	49	50	HDMI_TX1_D0P/EDP_TX1 _D0P	

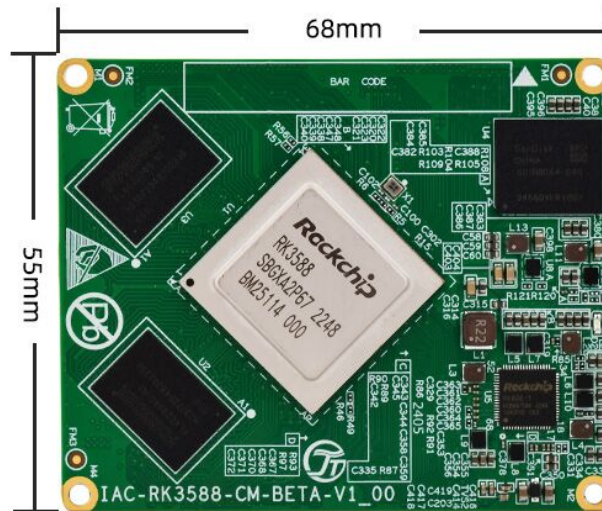
HDMI_TX0_D0N/EDP_TX 0_D0N	51	52	HDMI_TX1_D0N/EDP_TX 1_D0N
GND	53	54	GND
HDMI_TX0_D3P/EDP_TX0 _D3P	55	56	HDMI_TX1_D3P/EDP_TX1 _D3P
HDMI_TX0_D3N/EDP_TX 0_D3N	57	58	HDMI_TX1_D3N/EDP_TX 1_D3N
GND	59	60	GND
HDMI_TX0_SBDP/EDP_T X0_AUXP	61	62	HDMI_TX1_SBDP/EDP_T X1_AUXP
HDMI_TX0_SBDN/EDP_T X0_AUXN	63	64	HDMI_TX1_SBDN/EDP_T X1_AUXN
GND	65	66	GND
HDMI_TX0_SCL_M0	67	68	PCIE30X1_1_WAKEN_M1
HDMI_TX0_SDA_M0	69	70	PCIE30X1_1_CLKREQ_M 1
HDMI_TX0_CEC_M0	71	72	PCIE30X1_1_PERSTN_M 1
GPIO4_B5_d	73	74	UART0_TX_M2
GPIO4_B6_d	75	76	UART0_RX_M2
GPIO4_B4_u	77	78	GPIO4_A7_d
UART3_RX_M2	79	80	GND
UART3_TX_M2	81	82	HDMI_RX_D2P
GND	83	84	HDMI_RX_D2N
SDMMC_D3	85	86	HDMI_RX_D1P
SDMMC_D2	87	88	HDMI_RX_D1N
SDMMC_D1	89	90	HDMI_RX_D0P

	SDMMC_D0	91	92	HDMI_RX_D0N	
	SDMMC_CMD	93	94	GND	
	GND	95	96	HDMI_RX_CLKP	
	SDMMC_CLK	97	98	HDMI_RX_CLKN	
	GND	99	100	GND	

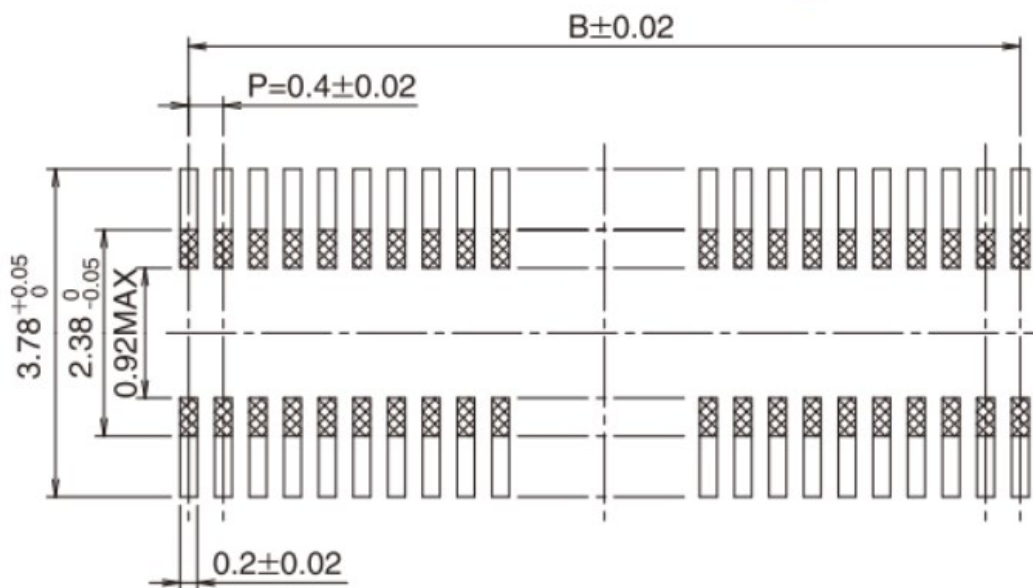
II. Size & Structure


Unit:mm, if needs the size of the board to board receptacle, please email to: supports@qiyangtech.com;

2.1. SOM Size



2.2. Board to Board receptacle size



No. of Contacts	A	B	C	D
30	8.6	5.6	1.5	2.9
40	10.6	7.6	3.2	
44	11.4	8.4		
50	12.6	9.6		
60	14.6	11.6		
70	16.6	13.6		
80	18.6	15.6		
90	20.6	17.6		
 100	22.6	19.6		

III. Electrical Characteristics

Item	Features
Working Temperature	0°C ~ +60°C (-40°C~+85°C optional)
Storage Temperature	-10°C ~ +85°C
Humidity	5% to 95%, non-condensation
SOM Size	68mm*55mm , 10-layer high precision immersion gold technology
Power consumption of the whole board	<3W (Non-Loaded)
Working Temperature	DC4V/2.5A

IV. Remark

1. Before connecting the LCD, please confirm the power specifications of your LCD module.

2. Please use the company's original connectors to avoid damage to the motherboard due to misconnection.

3. Our company shall provide E-mail, telephone and other communication technical support services and lifelong maintenance services for our products.

4. Our company promises to provide free maintenance service for our products within 6 months from the date of sale. If the user encounters a failure due to product quality problems during the use of our products, they can contact the seller or our company with the purchase receipt during the warranty period, and our company will be responsible for repairing the product for you or replacing it with a new one.

5. We shall not offer free repair under the following circumstances:

- expired warranty service period;
- no valid purchase receipt;
- the damage of the product was caused by liquid, moisture or mold;
- failures and damages caused by reasons other than product quality, such as falling, strong vibration, unauthorized modification, misoperation, etc. after purchase;
- damage caused by force majeure.

6. We reserve intellectual property for the software and hardware technical data of IAC-RK3588-CM; users can only use them for teaching, testing, researching. Shall not be engaged in any commercial purpose. Shall not distribute them on the Internet. Shall not intercept, modify them to tamper copyright.

7. We accept batch order and can offer technical support and service.

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