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FECHNOLOGY

### **Version Updates**

	Hardware			
Version	Platform	Description	Date	Revisor
1.0	IAC-A5D3x-Kit	Launched	2014-02-08	yao
2.0	IAC-A5D3x-Kit	Modify uboot, kernel, file system version	2014-10-08	WWX

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## Preface

Welcome to use the IAC-A5D3x-Kit from Hangzhou Qiyang

Intelligent Technology Company. It include four manuals in Linux:

IAC-A5D3x-Kit Linux User Manual. pdf

IAC-A5D3x-Kit Hardware Manual. pdf

IAC-A5D3x-Kit Linux Module Specification and Test Manual.

pdf

IAC-A5D3x-Kit Linux System Image Burning Manual. pdf

Hardware Part, you could refer to IAC-A5D3x-Kit Hardware

Manual. pdf

Mainboard Test, you could refer to IAC-A5D3x-Kit Linux Module

Specification and Test Manual. pdf

Image Burning, you could refer to IAC-A5D3x-Kit Linux System

Image Burning Manual.pdf

This manual is used for introducing how to set up the cross compiler, source code and application demo compiling.

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Before using this manual, please read *IAC-A5D3x-Kit Hardware Manual. pdf* carefully.

# **I**.Illustration

◆Build in Linux OS (ubuntu or other Linux release version). Operation Example: ubuntu 12.04. Installation, please refer to *Ubuntu Installation* 

### for Virtual Machine Manual.PDF

Copy file to virtual machine [ubuntu] while it is in compiling process,
 create a directory[mkdir~/work /\*], [~]means user catalogue;
 Absolute Path is[/home/st\*/].

All documentations are copied to this directory, users could create directory by themselves. Example:[~/work]

• Please refer to relevant materials about the common commands and vi operation in Linux.

• All of the copies of PC and virtual machine adopt samba shared access mode.

• Serial Connect: Connect Debugging UART Interface (J7) on development board to serial port on PC by serial port cable.

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◆ Network Connect: Connect Ethernet Interface (J16) to Network Interface on PC by network cable.

◆USB Connect: Connect USB Device(J22) on development board to USB on PC by USB cable

 Set Serial Port: Open terminal communication software(minicom or hyper terminal in PC),select baud rate [115200],stop bit [1], data bit [8], parity bit [none ] and data flow control[none]. Then test every serial port.

◆Find out the J1 jumper wire, you will need to use it in the following operation steps.

 Mainboard has CD catalogue, the tools software and code file are in the relative catalogue in CD. Please ensure that the materials are all in readiness.



	12 BOTTON	~	
퉬 Base Board PCB	2015/4/22 15:28	文件夹	
퉬 Compilation Tool	2015/4/22 15:30	文件夹	
퉬 Device Manual	2015/4/22 16:25	文件夹	
퉬 Image	2015/4/22 16:25	文件夹	
퉬 Schematic	2015/4/22 15:27	文件夹	
🐌 Source Code	2015/4/22 16:53	文件夹	
퉬 Structure & Size	2015/4/22 16:22	文件夹	
퉬 Test Code	2015/4/22 15:29	文件夹	
퉬 Tool Software	2015/4/22 16:21	文件夹	
퉬 ubuntu	2015/4/22 15:19	文件夹	
] User Manual	2015/4/22 16:50	文件夹	
퉬 Virtual Machine	2015/4/22 15:30	文件夹	

## **II. Burn Linux System Image**

If you need to re-burn Linux system, the development board provides two boot methods: dataflash and nandflash. Please select the most suitable boot method to burn, the specific method ,please refer to this manual:

### IAC-A5D3x-Kit Linux System Image Burning Manual. pdf

This manual introduces the burning method from dataflash and nandflash, and the method to update the image by network.

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# **III. Function and Test**

The development board file system has already integrated the test programs. After booting, you could find the corresponding test programs in [/usr/local/board\_test]directory. The specific test method ,you could refer to this manual:

IAC-A5D3x-Kit Linux Module Specification And Test Manual. pdf

## **IV. Install Cross Compiler Tool Chain**

[Bootloader],[kernel] and[fs] need cross-compiler to recompile.

All application programs and library files need cross-compiler to compile, they want to run on the mainboard. So, we will install the cross-compiler tool chain at first, there is a prepared cross-compiler tool in CD.

Users could use it directly. The GCC version is 4.7.3.

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				· · · · ·
.具(1) 带叫(H)				
共享 ▼ 新建文件夹				
名称	修改日期	类型	大小	
gcc-linaro-arm-linux-gnueabihf-4.7-2	2013/11/5 0:46	WinRAR 压缩文件	79,750 KB	

Next, we will introduce "How to install Cross-Compiler Tool

Chains?"

Copy[gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux.t

ar.bz2] cross-compiler tool chains to [~/work]directory.

st@st-virtual-machine:~/work\$ ls gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux.tar.bz2 st@st-virtual-machine:~/work\$

\$ tar -xjvf gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux.tar.bz2

Generate[gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_lin

ux] in current directory.

```
gnueabihf/4.7.3/liblto_plugin.so.0.00
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux/libexec/gcc/arm-linux-
gnueabihf/4.7.3/collect2
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux/libexec/gcc/arm-linux-
gnueabihf/4.7.3/liblto_plugin.so.0
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux/libexec/gcc/arm-linux-
gnueabihf/4.7.3/liblto_plugin.so
st@st-virtual-machine:~/work$ ls
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gnueabihf-4.7-2012.12-20121214_linux
grc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux.tar.bz2
st@st-virtual-machine:~/work$
```

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Add this cross-compilers' path to system environment variables

[PATH], and add into the current user's [bash.bashrc].

\$ vi ~/.bashrc

Add the following path in the file.



Save & Exit!

Make the new environment variables effective.

\$ source ~/.bashrc

After the environment variables taking effect, next, we confirm whether

the cross-compiler has been installed successfully:

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😣 🗖 🗊 st@st-virtual-machine: ~/work st@st-virtual-machine:~/work\$ arm-linux-gnueabihf-gcc -v 使用内建 specs。 COLLECT\_GCC=arm-linux-gnueabihf-gcc COLLECT LTO WRAPPER=/home/st/work/gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-201 21214\_linux/bin/../libexec/gcc/arm-linux-gnueabihf/4.7.3/lto-wrapper 目标: arm-linux-gnueabihf 配置为: /cbuild/slaves/oorts/crosstool-ng/builds/arm-linux-gnueabihf-linux/.buil d/src/gcc-linaro-4.7-2012.12/configure --build=i686-build\_pc-linux-gnu --host=i6 86-build\_pc-linux-gnu --target=arm-linux-gnueabihf --prefix=/cbuild/slaves/oorts /crosstool-ng/builds/arm-linux-gnueabihf-linux/install --with-sysroot=/cbuild/sl aves/oorts/crosstool-ng/builds/arm-linux-gnueabihf-linux/install/arm-linux-gnuea bihf/libc --enable-languages=c,c++,fortran --enable-multilib --with-arch=armv7-a --with-tune=cortex-a9 --with-fpu=vfpv3-d16 --with-float=hard --with-pkgversion= 'crosstool-NG linaro-1.13.1-4.7-2012.12-20121214 - Linaro GCC 2012.12' --with-bu gurl=https://bugs.launchpad.net/gcc-linaro --enable-\_\_cxa\_atexit --enable-libmud flap --enable-libgomp --enable-libssp --with-gmp=/cbuild/slaves/oorts/crosstoolng/builds/arm-linux-gnueabihf-linux/.build/arm-linux-gnueabihf/build/static --wi th-mpfr=/cbuild/slaves/oorts/crosstool-ng/builds/arm-linux-gnueabihf-linux/.buil d/arm-linux-gnueabihf/build/static --with-mpc=/cbuild/slaves/oorts/crosstool-ng/ builds/arm-linux-gnueabihf-linux/.build/arm-linux-gnueabihf/build/static --withppl=/cbuild/slaves/oorts/crosstool-ng/builds/arm-linux-gnueabihf-linux/.build/ar m-linux-gnueabihf/build/static --with-cloog=/cbuild/slaves/oorts/crosstool-ng/bu ilds/arm-linux-gnueabihf-linux/.build/arm-linux-gnueabihf/build/static --with-li belf=/cbuild/slaves/oorts/crosstool-ng/builds/arm-linux-gnueabihf-linux/.build/a rm-linux-gnueabihf/build/static --with-host-libstdcxx='-L/cbuild/slaves/oorts/cr osstool-ng/builds/arm-linux-gnueabihf-linux/.build/arm-linux-gnueabihf/build/sta tic/lib -lpwl' --enable-threads=posix --disable-libstdcxx-pch --enable-linker-bu ild-id --enable-gold --with-local-prefix=/cbuild/slaves/oorts/crosstool-ng/build s/arm-linux-gnueabihf-linux/install/arm-linux-gnueabihf/libc --enable-c99 --enab le-long-long --with-mode=thumb 线程模型: posix gcc 版本 4.7.3 20121205 (prerelease) (crosstool-NG linaro-1.13.1-4.7-2012.12-201 21214 - Linaro GCC 2012.12) st@st-virtual-machine:~/work\$

As shown, GCC version is 4.7.3.

So far, the cross-compiler is totally installed, then we could use it to

compile system code and application program.

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# V. Compile Bootstrap

Bootstrap is the secondary bootstrap program, the primary bootstrap program has been cured into the ROM of the CPU chip. The users could not modify!

The bootstrap is in charge of the initialization of clock, GPIO, memory etc. IAC-A5D3x-Kit development board provides three boot methods base on the different image mediums.

- dataflash boot method
- nandflash boot method
- SD Card boot method

Base on the internal program, CPU will search for boot code base on the storage medium by the following priority :

### Dataflash>nandflash>SD Card

Before selecting boot method, please make sure that there is no higher level boot medium or the medium has been interrupted. Or the CPU will use the higher level boot method.

The compiled bootstrap image file is in the CD, you can use it

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directly. If you want to recompile, the migrated bootstrap source code is

ready too.

共享 新建文件夹			
你 *	修改日期	英型	大小
at91bootstrap-3.5.4_2013.12.27_v1.0.t	2014/10/10 15:20	WinRAR 压缩文件	2,067 KB

Copy the [bootstrap] source code to [~/work]directory in CD, then

extract:

\$ tar -xzvf at91bootstrap-3.5.4\_2013.12.27\_v1.0.tar.gz



After extracting, there is a [at91bootstrap-3.5.4] folder, enter into

this folder:

\$ cd at91bootstrap-3.5.4

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st@st-virtual-machine:~/w	ork\$ ls	-	
at91bootstrap-3.5.4			
$at91bootstrap_3 5 4 2013$	12 27 v1 0 tar az		
ac91000cscrap-5.5.4_2015.		404044 14.000	
gcc-linaro-arm-linux-gnue	abint-4.7-2012.12-20	121214_UUNUX	
gcc-linaro-arm-linux-gnue	abiht-4.7-2012.12-20	121214_Linux.t	ar.bz2
st@st-virtual-machine:~/w	ork\$ cd at91bootstra	p-3.5.4/	
st@st-virtual-machine:~/w	ork/at91bootstrap-3.	5.4\$ ls	
board config	elf32-littlearm.lds	KNOWN_ISSUES	README.txt
build_df.sh Config.in	fs	lib	scripts
<pre>build_nf.sh crt0_gnu.S</pre>	host-utilities	main.c	toplevel_cpp.mk
build_sd.sh driver	include	Make <u>f</u> ile	
st@st-virtual-machine:~/w	ork/at91bootstrap-3.	5.4\$	

As shown: In order to compile easily, we have already made the corresponding compile steps of the boot method into a script file.

build\_df.sh It is the bootstrap script file for dataflash boot
 method

• build\_nf.sh It is the bootstrap script file for nandflash.

• build\_sd.sh It is the bootstrap script file for SD Card.

Users could select the corresponding script file to compile, here is an example : dataflash. The other boot methods' compiling method will not be illustrated again. Execute [build\_df.sh]script file.

\$./build\_df.sh

Execute the above script file, the interface shows the following message :

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st@st-virtual-machine: ~/work/at91bootstrap-3.5.4
 at91bootstrap vBR2\_VERSION Configuration config t91bootstrap Configuration Arrow keys navigate the menu. <Enter> selects submenus --->. Highlighted letters are hotkeys. Pressing <Y> selectes a feature, while <N> will exclude a feature. Press <Esc><Esc> to exit, <?> for Help, </>> for Search. Legend: [\*] feature is selected [] feature is (at91sama5d3xek) Board Name Board Type (at91sama5d3xek) ---> Crystal Frequency (Build for use of an 12.000 MHz crystal) CPU clock (528 MHz) Bus Speed (133 MHz) Memory selection Image Loading Strategy (Load U-Boot into last MBYTE of SDRAM)
U-Boot Image Storage Setup --->
Perform a memory test at startup [\*] Debug Support Debug Level (General debug information) [\*] Call Hardware Initialization Call User specific Hardware Initialization Use external 32KHZ oscillator as source of slow clock [\*] Disable Watchdog Load an Alternate Configuration File Save an Alternate Configuration File <Select> < Exit > < Help >

User could configure as requirement, after configuring, select [yes] to save, then exit. If it is no need to modify, then [exit] by default. The IAC-A5D3x-Kit default configuration, you should select the [Memory Selection] to 256M.

Memory selection>	
RAM size (512 MB)	>

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⊗
.config - at91bootstrap vBR2_VERSION Configuration
RAM size
Use the arrow keys to navigate this window or press the hotkey of the item you wish to select followed by the <space bar="">. Press</space>
<pre><?> for additional information about this option.</pre>
() 32 MB
() 04 MB () 128 MB
(X) 256 MB () 512 MB
Select then [exit]
bereet, then [exit].
st@st-virtual-machine: ~/work/at91bootstrap-3.5.4
.config - at91bootstrap vBR2_VERSION Configuration
- Memory selection
Arrow keys navigate the menu. <enter> selects submenus&gt;.</enter>
while <n> will exclude a feature. Press <esc> to exit, <? > for</esc></n>
Help, > for Search. Legend: [*] feature is selected [] feature is
RAM size (256 MB)>
SPI configuration>
<pre><select> &lt; Exit &gt; &lt; Help &gt;</select></pre>

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Select [Yes], then save.

Finish configuring, then execute [make] command to compile.

\$ make

Finish compiling, there is no error.

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🛞 🗇 🗊 st@st-virtual-machine: ~/work/at91bootstrap-3.5.4
G_CPU_CLK_528MHZ -DCONFIG_BUS_SPEED_133MHZ -DCONFIG_HAS_PIO3 -DCONFIG_LOAD_ONE_WI
FTC SMALL DATAFLASH -DATA1C ST CLK-33000000 -DATAFLASH -DATAFLASH -DATAFLASH
PCS® DATAFLASH - DBOOTSTRAP DEBLIG LEVEL=DEBLIG INFO - DCONFIG DISABLE WATCHDOG
ld FLAGS
-nostartfiles -Map=/home/st/work/at91bootstrap-3.5.4/binaries/at91sama5d3xek-data
flashboot-uboot-3.5.4.mapcref -static -T elf32-littlearm.ldsgc-sections -Tt
ext 0x300000
AS /home/st/work/at91bootstrap-3.5.4/crt0_gnu.S
CC /home/st/work/at91bootstrap-3.5.4/main.c
CC /home/st/work/at91bootstrap-3.5.4/board/at91sama5d3xek/at91sama5d3xek
•C
CC /home/st/work/at91bootstrap-3.5.4/lib/string.c
CC /home/st/work/at91bootstrap-3.5.4/lib/eabi_utils.c
CC /home/st/work/at91bootstrap-3.5.4/lib/div.c
CC /home/st/work/at91bootstrap-3.5.4/driver/debug.c
CC /home/st/work/at91bootstrap-3.5.4/driver/at91_slowclk.c
CC /home/st/work/at91bootstrap-3.5.4/driver/at91_pio.c
CC /home/st/work/at91bootstrap-3.5.4/driver/pmc.c
CC /home/st/work/at91bootstrap-3.5.4/driver/at91_pit.c
CC /home/st/work/at91bootstrap-3.5.4/driver/at91_wdt.c
CC /home/st/work/at91bootstrap-3.5.4/driver/dbgu.c
CC /home/st/work/at91bootstrap-3.5.4/driver/ddramc.c
CC /home/st/work/at91bootstrap-3.5.4/driver/at91_spi.c
CC /home/st/work/at91bootstrap-3.5.4/driver/dataflash.c
CC /home/st/work/at91bootstrap-3.5.4/driver/ds24xx.c
mkdir -p /home/st/work/at91bootstrap-3.5.4/binaries
LD at91sama5d3xek-dataflashboot-uboot-3.5.4.elf
Size of at91sama5d3xek-dataflashboot-uboot-3.5.4.bin is 4280 bytes
[Succeeded] It's OK to fit into SRAM area
st@st-vtrtual-machine:~/work/at91bootstrap-3.5.45

There is an executable file

[at91sama5d3xek-dataflashboot-uboot-3.5.4.bin] in [binaries] directory.

	LL S UN LU L						
st@st-virtua	l-machine:~/	work/at91bootstrap-3.	5.4Ş ls				
binaries	config	elf32-littlearm.lds	lib	scripts			
board	Config.in	fs	main.c	toplevel_cpp.mk			
build_df.sh	crt0_gnu.o	host-utilities	main.o				
build_nf.sh	crt0_gnu.S	include	Makefile				
build_sd.sh	driver	KNOWN_ISSUES	README.txt				
st@st-virtual-machine:~/work/at91bootstrap-3.5.4\$ ls binaries							
at91sama5d3xek-dataflashboot-uboot-3.5.4.bin							
at91sama5d3xek-dataflashboot-uboot-3.5.4.elf							
at91sama5d3x	at91sama5d3xek-dataflashboot-uboot-3.5.4.map						
st@st-virtua	l-machine:~/	work/at91bootstrap-3.	5.4\$				

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# **VI.** Compile uboot

There is a prepared source code [uboot] ,user could compile it

directly.

▶ IAC-A5D3X文档 ▶ IAC-A5D3x-Kit ▶ lir	ux 🕨 Source Code	▶ System Code ▶	Bootstrap Sou	irce 🕨 u-boot		
工具(T) 帮助(H)						
共享 ▼ 新建文件夹						
名称	修改日期	类型	大小			
📜 u-boot-at91-2012.10_v1.1.20140926.tar	2014/10/10 15:20	WinRAR 压缩文件	15,621 KB			

Copy the [u-boot]source code to [~/work]directory in CD, and use

the following command to extract:

\$ tar -xzvf u-boot-at91-2012.10\_2013.12.27\_v1.0.tar.gz

After extracting ,there is a folder [u-boot-at91-2012.10], enter into

this folder.

```
$ cd u-boot-at91-2012.10
```

\$ <mark>1</mark>s

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```
u-boot-at91-2012.10/fs/yaffs2/yaffs_nandif.c
u-boot-at91-2012.10/fs/yaffs2/yaffs_ecc.c
u-boot-at91-2012.10/fs/yaffs2/yaffs_attribs.h
u-boot-at91-2012.10/CREDITS
u-boot-at91-2012.10/.gitignore
u-boot-at91-2012.10/config.mk
u-boot-at91-2012.10/MAKEALL
u-boot-at91-2012.10/README
u-boot-at91-2012.10/boards.cfg
st@st-virtual-machine:~/work$ ls
at91bootstrap-3.5.4
at91bootstrap-3.5.4_2013.12.27_v1.0.tar.gz
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux.tar.bz2
u-boot-at91-2012.10
u-boot-at91-2012.10_2013.12.27_v1.0.tar.gz
st@st-virtual-machine:~/work$ ^C
st@st-virtual-machine:~/work$ cd u-boot-at91-2012.10/
st@st-virtual-machine:~/work/u-boot-at91-2012.10$ ls
api
             build_nf.sh CREDITS env.txt
                                             MAINTAINERS net
                                                                            spl
arch
             build sd.sh disk
                                   examples
                                            MAKEALL
                                                                            test
                                                          post
                                             Makefile
board
             common
                                                          README
                                                                            tools
                          doc
                                   fs
boards.cfg
             config.mk
                          drivers include
                                             mkconfig
                                                          rules.mk
build_df.sh COPYING
                          dts
                                   lib
                                             nand_spl
                                                          snapshot.commit
st@st-virtual-machine:~/work/u-boot-at91-2012.10$
```

As shown: In order to compile easily, we have already made the corresponding compilation steps of the boot method into a script file.

build\_sd.sh It is the bootstrap script file for dataflash.

- build\_nf.sh It is the bootstrap script file for nandflash.
- build\_sd.sh It is the bootstrap script file for SD Card.

User could select the corresponding script file to compile, here is the example : dataflash. The other boot method compiling method will be

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same. Execute [build\_df.sh]script file.

#### \$ ./build\_df.sh

There is no error in compiling, so there will be an image file [uboot.bin]

in current directory which could be burnt into the development board.

\$ <mark>1s</mark>

🛞 🗇 💿 st@st-virtual-machine: ~/work/u-boot-at91-2012.10

d.o drivers/mtd/nand/libnand.o drivers/mtd/onenand/libonenand.o drivers/mtd/spi/l ibspi_flash.o drivers/mtd/ubi/libubi.o drivers/net/libnet.o drivers/net/phy/libph y.o drivers/pci/libpci.o drivers/pcmcia/libpcmcia.o drivers/power/libpower.o driv ers/rtc/librtc.o drivers/serial/libserial.o drivers/spi/libspi.o drivers/twserial /libtws.o drivers/usb/eth/libusb_eth.o drivers/usb/gadget/libusb_gadget.o drivers /usb/host/libusb_host.o drivers/usb/musb/libusb_musb.o drivers/usb/phy/libusb_phy .o drivers/usb/ulpi/libusb_ulpi.o drivers/video/libvideo.o drivers/watchdog/libwa tchdog.o fs/cramfs/libcramfs.o fs/ext4/libext4fs.o fs/fat/libfat.o fs/fdos/libfds s.o fs/jffs2/libjfs2.o fs/reiserfs/libreiserfs.o fs/ubifs/libubifs.o fs/yaffs2/l ibyaffs2.o fs/zfs/libzfs.o lib/libfdt/libfdt.o lib/libgeneric.o lib/lzma/liblzma. o lib/lzo/liblzo.o lib/zlib/libz.o net/libnet.o post/libpost.o test/libtest.o boa rd/atmel/sama5d3xek/libsama5d3xek.oend-group /home/st/work/u-boot-at91-2012.10 /arch/arm/lib/eabi_compat.o -L /home/st/work/gcc-linaro-arm-linux-gnueabihf-4.7-						
-map -0 u-boo						
arm-linux-gnu	jeabihf-obj	copy -0 srec u	1-poot n-po	bot.srec		
arm-linux-gnu	ueabihf-obj	copygap-fil	ll=0xff -0	binary u-boot u-l	boot.bin	
make -C examp	oles/standa	lone all				
make[1]: 正在	进入目录	/home/st/work/	/u-boot-ats	91-2012.10/example	es/standalone'	
make[1]: 没有	什么可以做	的为 `all'。		and a second state of the second	entered and a second second	
make[1]:正在	离开日录 /	home/st/work/	i-boot-at9	1-2012.10/example	s/standalone'	
make - C examp	les/ani al			Louistio/cxdipte.		
	: 進入日表、	/home/st/work	/u-boot-ate	01-2012 10/example	es/ani'	
				51-2012.10/exampte	esyape	
		all's	-		1	
make[1]:止住	为力日求 /	nome/st/work/u	J-Doot-at9	1-2012.10/example:	s/apt'	
st@st-virtual	l-machine:~	/work/u-boot-a	at91-2012.	loş ls	0.0000000000000000000000000000000000000	
api	common	dts	MAKEALL	rules.mk	u-boot.bin	
arch	config.mk	env.txt	Makefile	<pre>snapshot.commit</pre>	u-boot.lds	
board	COPYING	examples	mkconfig	spl	u-boot.map	
boards.cfg	CREDITS	fs	nand_spl	System.map	u-boot.srec	
build_df.sh	disk	include	net	test		
build_nf.sh	doc	lib	post	tools		
build sd.sh	drivers	MAINTAINERS	README	u-boot		
st@st-virtual-machine:~/work/u-boot-at91-2012.10\$						

\*Note: When it is in the burning process, please make sure that the

[uboot] and [bootstrap] image are compiled and obtained by the

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same boot method.

# VII. Compile Kernel

There is a migrated and compiled kernel source file in CD.

5 🕨 IAC-A5D3X文档 🕨 IAC-A5D3x-Kit 🕨 li	inux 🕨 Source Code	► System Code ►	Kernel Source	
工具(T) 帮助(H)				
共享 ▼ 新建文件夹				
名称	修改日期	类型	大小	
linux-at91-3.6.9_v1.1.20140926.tar mkimage	2014/10/10 15:20 2014/10/10 15:20	WinRAR 压缩文件 文件	105,212 КВ 52 КВ	

Illustration:[linux-at91-3.6.9\_2014.2.8\_v2.1.tar.gz] is the kernel source code.

[mkimage] is the tool to generate uImage.

Copy [linux-at91-3.6.9\_2014.2.8\_v2.1.tar.gz] and [mkimage] in CD into

[~/work]directory, then extract kernel source code.

\$ tar -xzvf linux-at91-3.6.9\_2014.2.8\_v2.1.tar.gz

After extracting , generate [linux-at91-3.6.9]folder in current directory,

enter into this folder.

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\$ cd linux-at91-3.6.9

#### \$ <mark>1s</mark>

😣 🖻 🗊 st@st-	virtual-machine: ~/	work/linux-at91-3	3.6.9			
linux-at91-3. linux-at91-3. linux-at91-3. linux-at91-3. linux-at91-3. linux-at91-3.	5.9/virt/kvm/ 5.9/virt/kvm/asy 5.9/virt/kvm/Kcc 5.9/virt/kvm/eve 5.9/virt/kvm/kvm	/nc_pf.h onfig entfd.c n_main.c				
linux-at91-3.6.9/virt/kvm/iommu.c						
linux-at91-3.6.9/virt/kvm/coatesced_mmlo.c						
linux-at91-3.6.9/virt/kvm/assigned-dev.c						
linux-at91-3.6.9/virt/kvm/coalesced mmio.h						
linux-at91-3.6.9/virt/kvm/ioapic.c						
linux-at91-3.6.9/virt/kvm/ioapic.h						
linux-at91-3.6.9/virt/kvm/irq_comm.c						
linux-at91-3.6.9/virt/kvm/async_pf.c						
linux-at91-3.6.9/README						
st@st-virtual-machine:~/work\$ ls						
at91bootstrap-3.5.4						
at91D00tstrap-3.5.4_2013.12.27_v1.0.tar.gz						
gcc-llnaro-arm-llnux-gnueablhr-4./-2012.12-20121214_llnux						
gcc-ccnaro-arm-ccnux-gnueabcnr-4.7-2012.12-20121214_ccnux.car.022						
linux-at91-3.6.9 2014.01.03 v1.0.tar.oz						
mkimage						
u-boot-at91-20	012.10					
u-boot-at91-2012.10_2013.12.27_v1.0.tar.gz						
st@st-virtual-machine:~/work\$ cd linux-at91-3.6.9/						
st@st-virtual	-machine:~/work/	linux-at91-3.	6.9\$ ls			
arch	Documentation	ipc	Makefile	samples	virt	
block	drivers	Kbuild	mm	scripts		
build_dts.sh	firmware	Kconfig	Module.symvers	security		
COPYING	TS	Kernel	NET	sound		
CREDITS	init	MATNTATNEDS		LOOLS		
stost-victual	-machinet~/work	/linux-at91-3	6 9S	usi		
st@st-virtual	-machine:~/work/	linux-at91-3.	6.95			

After compiling, it needs the following command to configure

kernel.

\$ make menuconfig

After executing, it pop-up the following kernel selection configuration interface.

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⊗⊜  st@st-virtual-machine: ~/work/linux-at91-3.6.9
.config - Linux/arm 3.6.9 Kernel Configuration
Linux/arm 3.6.9 Kernel Configuration Arrow keys navigate the menu. <enter> selects submenus&gt;. Highlighted letters are hotkeys. Pressing <y> includes, <n> excludes, <m> modularizes features. Press <esc> to exit, <? > for Help,  for Search. Legend: [*] built-in [] excluded <m> module &lt;&gt; module</m></esc></m></n></y></enter>
<pre>(*) Patch physical to virtual translations at runtime General setup&gt; [*] Enable loadable module support&gt; [*] Enable the block layer&gt; System Type&gt; Bus support&gt; Kernel Features&gt; Boot options&gt; CPU Power Management&gt; Floating point emulation&gt; Userspace binary formats&gt; Power management options&gt; [*] Networking support&gt; Pevice Drivers&gt; File systems&gt; Kernel hacking&gt; Security options&gt; -*- Cryptographic API&gt; Library routines&gt;</pre>
<pre><select> &lt; Exit &gt; &lt; Help &gt;</select></pre>

The users could modify the kernel function options. The other configurations will be illustrated here. According to the specific requirements ,the users could configure it. If there are no special requirements, you could compile kernel by the default kernel option. Here ,we take an sample for modifying kernel resolution.

#### LCD resolution select configuration path in kernel option:

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Device Drivers --->

Graphics supports --->

Support for frame buffer devices --->

AT91 frame buffer display (LCD 1024X768 display)

st@st-virtual-machine: ~/work/linux-at91-3.6.9 00

Use the arrow the item you for addit	keys to navigate this window or press the hot wish to select followed by the <space bar="">. Pro ional information about this option.</space>	key of ess
	<ul> <li>( ) LCD 480X272 display</li> <li>( ) LCD 640X480 display</li> <li>( ) LCD 800X480 display</li> <li>( ) LCD 800X600 display</li> <li>( ) LCD 1024X768 display</li> </ul>	
	<select> &lt; Help &gt;</select>	

Modify the default configuration from [LCD 800X600 display]

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to [LCD 1024X768 display], then save & exit.

Start to compile.

\$ make

The first compilation may need 8-10 minutes, here we take a Computer /dual core 2.4GHz to install virtual machine as a reference. The different computer or server may have some difference in compilation time.

Start to compile kernel image

\$ make uImage

After executing, it hints the following errors:

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⊗ 🗇 🗊 st@st-virtual-machine: ~/work/linux-at91-3.6.9	
st@st-virtual-machine:~/work/linux-at91-3.6.9\$	
st@st-virtual-machine:~/work/linux-at91-3.6.95	
st@st-virtual-machine:~/work/linux-at91-3.6.95	
st@st-virtual-machine:~/work/linux-at91-3.6.9\$	
st@st-virtual-machine:~/work/linux-at91-3.6.95	
st@st-virtual-machine:~/work/linux-at91-3.0.95	
Store of the second sec	
make[1]: "include/generated/mastelease.h"是是新的	
CALL scripts/checksyscalls sh	
CHK include/depended/compile b	
Kernel: arch/arm/boot/Image is ready	
Kernel: arch/arm/boot/zTmage is ready	
UIMAGE arch/arm/boot/uImage	
"mkimage" command not found - U-Boot images will not be built	
make[1]: *** [arch/arm/boot/uImage] 错误 1	
make: *** [uImage] 错误 2	
st@st-virtual-machine:~/work/linux-at91-3.6.95	

The above picture hints that it lacks [mkimage]command, it needs the [mkimage]tool to generate the kernel image. We have copied the [mkimage]tool to work directory. Now, we need to add it to the system environment variables. So that, the system could call automatically.

It is simple now.

Copy [mkimage] to [bin] directory in Cross-Compiler.

\$ cp ../mkimage ~/work/gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux/bin/

Now, we could execute the compiling command, it could compile kernel image smoothly.

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\$ make uImage

st@st-virtual-machine:~/work/linux-at91-3.6.9\$ cp/mkimage ~/work/gcc-linaro-ar
m-linux-gnueabihf-4.7-2012.12-20121214_linux/bin/
st@st-virtual-machine:~/work/linux-at91-3.6.9\$ make uImage
CHK include/linux/version.h
CHK include/generated/utsrelease.h
make[1]: "include/generated/mach-types.h"是最新的。
CALL scripts/checksyscalls.sh
CHK include/generated/compile.h
Kernel: arch/arm/boot/Image is ready
Kernel: arch/arm/boot/zImage is ready
UIMAGE arch/arm/boot/uImage
Image Name: Linux-3.6.9
Created: Wed Jan 22 10:53:05 2014
Image Type: ARM Linux Kernel Image (uncompressed)
Data Size: 2643344 Bytes = 2581.39 kB = 2.52 MB
Load Address: 20008000
Entry Point: 20008000
Image arch/arm/boot/uImage is ready
st@st-virtual-machine:~/work/linux-at91-3.6.9\$

After compiling, it will generate [uImage] kernel image file in [arch/arm/boot/] directory which could be burnt into the development board.

image arch/arm/bool/ulmage is ready
st@st-virtual-machine:~/work/linux-at91-3.6.9\$ ls arch/arm/boot/
bootp compressed dts Image install.sh Makefile tftpd32.exe uImage zImage
st@st-virtual-machine:~/work/linux-at91-3.6.9\$

After compiling, there is a recompiled kernel image [uImage] in [arch/arm/boot/]directory.

After compiling [uImage], we could compile device tree image.

\$ make ARCH=arm CROSS\_COMPILE=arm-linux-gnueabihf- dtbs

After compiling, there is sama5d3x series device tree in [arch/arm/boot/]directory. The [sama5d3xek.dtb] is the device tree image. [X] means the corresponding number which use the current using

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CPU model.

For example, after compiling, there is a device tree image [sama5d34ek.dtb] which sama5d34 compiled.



# VII. File System Creation

There is a prepared file system code and creation tool in CD.

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酒(V) 工具(T) 帮助(H)				
新建文件夹				
名称	修改日期	类型	大小	
nkfs.ubifs	2014/10/10 15:20	UBIFS 文件	270 KB	
🔚 rootfs_v1.1.20140926.tar	2014/10/10 15:20	WinRAR 压缩文件	44,569 KB	
ubinize	2014/10/10 15:20	文件	116 KB	
ubinize.cfg	2014/10/10 15:20	CFG 文件	1 KB	

Copy file code creation system source and tool to [~/work]directory, then extract.

File system source code needs [root] permission to do completed

extraction.

CHNOI Add [sudo] before the extracting command.

\$ sudo tar -xzvf rootfs-2014.2.8\_v2.1.tar.gz

After extracting, it generate [rootfs]folder.

```
st@st-virtual-machine:~/work$ ls
at91bootstrap-3.5.4
at91bootstrap-3.5.4_2013.12.27_v1.0.tar.gz
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux
gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214_linux.tar.bz2
linux-at91-3.6.9
linux-at91-3.6.9_2014.01.03_v1.0.tar.gz
mkfs.ubifs
nkimage
rootfs
rootfs-2014.2.8_v2.1.tar.gz
ubinize
ubinize.cfg
u-boot-at91-2012.10
u-boot-at91-2012.10_2013.12.27_v1.0.tar.gz
st@st-virtual-machine:~/work$
```

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Copy creation tool [ubinize]and [mkfs.ubifs] to [bin]directory in

cross-compiler.

\$ cp ubinize ~/work/gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux/bin/

\$ cp mkfs.ubifs ~/work/gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux/bin/

Generate [rootfs.ubifs].

\$ sudo mkfs.ubifs -r rootfs -m 2048 -e 126976 -c 1816 -o rootfs.ubifs

Generate [rootfs.ubi].

\$ sudo ubinize -o rootfs.ubi -m 2048 -p 128KiB -s 2048 ./ubinize.cfg

st@st-virtual-machine:~/work\$ sudo mkfs.ubifs -r rootfs -m 2048 -e 126976 -c 181 6 -o rootfs.ubifs st@st-virtual-machine:~/work\$ sudo ubinize -o rootfs.ubi -m 2048 -p 128KiB -s 20 48 ./ubinize.cfg st@st-virtual-machine:~/work\$ ls at91bootstrap-3.5.4 at91bootstrap-3.5.4\_2013.12.27\_v1.0.tar.gz gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux gcc-linaro-arm-linux-gnueabihf-4.7-2012.12-20121214\_linux.tar.bz2 linux-at91-3.6.9 linux-at91-3.6.9\_2014.01.03\_v1.0.tar.gz mkfs.ubifs mkimage rootfs rootfs-2014.2.8\_v2.1.tar.gz rootfs.ubi rootfs.ubifs ubinize ubinize.cfg u-boot-at91-2012.10 u-boot-at91-2012.10\_2013.12.27\_v1.0.tar.gz st@st-virtual-machine:~/work\$

The generated [rootfs.ubi] is the [ubi]file system image which could

be burnt into development board.

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## **IX. Application Program Development**

You could develop the application program on Personal Computer, here, we take [Hello World] as an sample. Create [app]folder in [~/work] directory, enter into [app] folder.

\$ mkdir app

\$ cd app

At first, program a [Hello World] code as follows:

#include <stdio.h>

int main(void)

{

printf("Hello World ! \n");

return 0;

}

Save it to [hello.c]file.



Enable the program to run on the development board. It needs to use

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 %



the installed cross-compiler to compile the application programs.

Here is the compiling command:

\$ arm-linux-gnueabihf-gcc –o hello hello.c

\$ file hello

st@st-virtual-machine:~/work/app\$ arm-linux-gnueabihf-gcc -o hello hello.c
st@st-virtual-machine:~/work/app\$ ls
hello hello.c
st@st-virtual-machine:~/work/app\$ file hello
hello: ELF 32-bit LSB executable, ARM, version 1 (SYSV), dynamically linked (use
s shared libs), for GNU/Linux 2.6.31, BuildID[sha1]=0x31f8d5b22d847b154ef6a69a87
bd2d3091aaddd9, not stripped
st@st-virtual-machine:~/work/app\$

After compiling, there is an executable binary file [hello] in current directory.

Then, we could copy the executable program [hello] into development board by SD Card, USB flash disk, tftp or nfs mounting method. Then you could execute [hello] program in development board.

# X. Conclusion

The above contents may not be detailed enough, if you have any technical problems or suggestion, please contact us: supports@qiyangtech.com. Or you could log in our company forum to contact: http://www.qiytech.com. For more information, please contact: sales@qiyangtech.com , or log in http://www.qiytech.com/index.html.

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