



# IAC-IMX6-Kit Hardware Manual

Version:2.0  
2021.08

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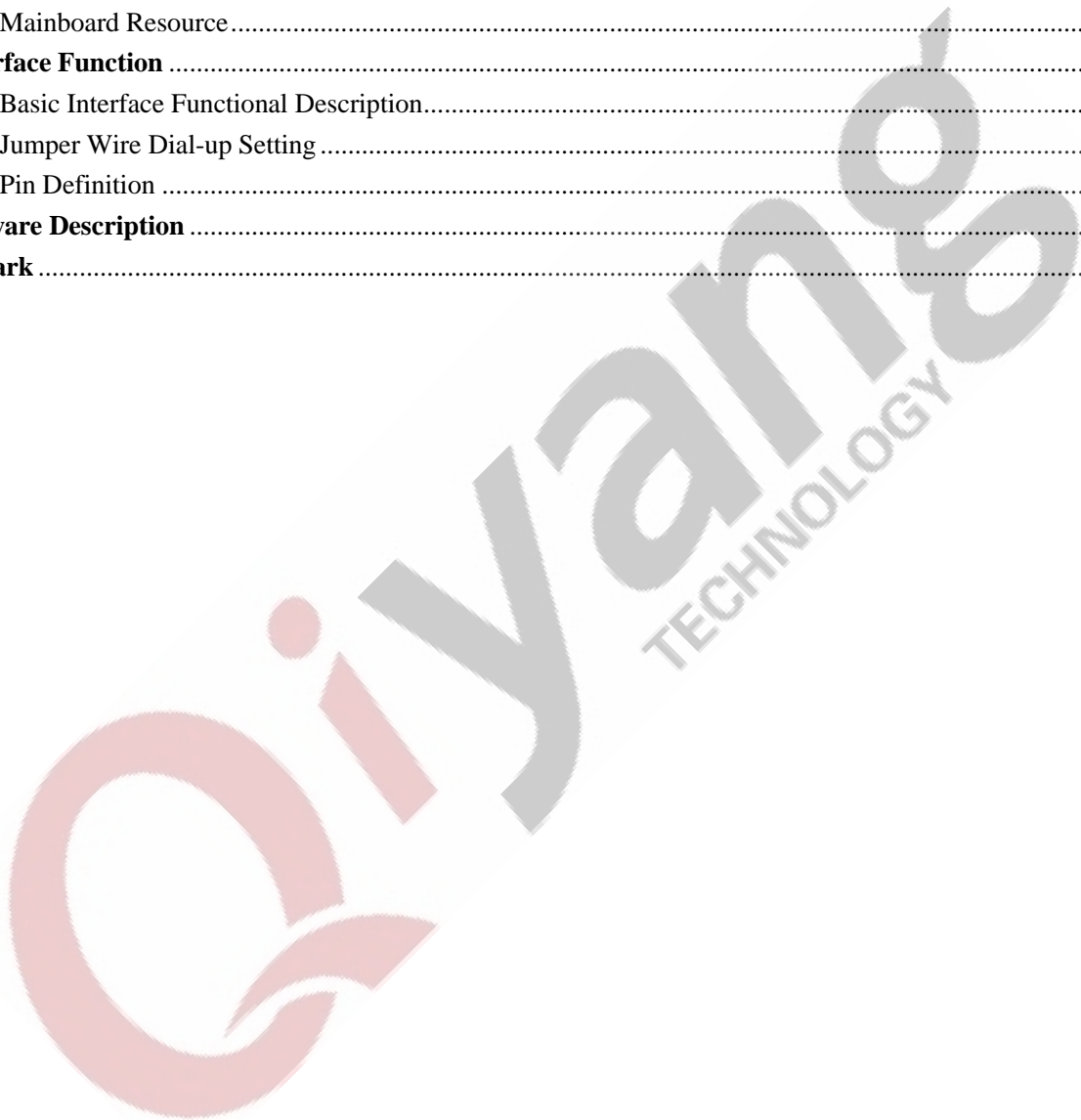
## Version Update

Version	Hardware	Description	Date	Reviser
1.0	IAC-IMX6-Kit	Launched	2014-03-16	
2.0	IAC-IMX6-Kit	Change Picture	2021-08	WWX



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**Notice: This manual introduces the hardware interface of the mainboard.**

## I. Preface

### 1.1 Company Profile

Zhejiang Qiyang Intelligent Technology Co., Ltd. Was estimated in Hangzhou in 2007. It is a National High-tech Enterprise focusing on the development, production and sales of ARM embedded products. More than 10 years of accumulation and precipitation have successfully constructed a service chain from product development to mass production.

As the core of the company, Qiyang R&D team is composed of more than 30 embedded engineers, who committed to providing users with easy-to-use embedded hardware, software tools and customized product solutions. Our products and solutions have been widely used in industrial control, Internet of Things, new retail, medical, electricity, environmental monitoring, charging piles and other fields.

The production base established in Zhuji provides a strong guarantee for Qiyang. It covers a over 5,000 square meters area also has 2 SMT production lines, and has passed and strictly followed the ISO9001 quality management system certification to guide production. With the strong production strength, the annual output can reach 1

million sets, ensuring the delivery time and solving the worries of users.

Qiyang has a complete sales market network, professional sales and after-sales team providing users with a full range of technical support and services. The business has spread to more than 120 countries and regions, and has successfully helped more than 2,000 users to quickly and efficiently bring products to the market.

The combination and extension of R&D, production capacity and market has laid a solid foundation for Qiyang to become a professional and global supplier of embedded software and hardware.

We offer:

- **Multi-platform software/hardware products**

NXP, Rockchip, MTK, Renesas, TI, Atmel, Cirrus Logic and other multi-platform ARM development boards/core boards/industrial control boards and peripheral hardware products, as well as supporting tools and software resources to support users' rapid secondary development.

- **Customized service**

Give full play to the accumulation of technology on the ARM platform, Linux, Android, and Ubuntu operating systems to provide users with customized embedded product services (OEM/ODM).

Thank you for using Qiyang Intelligent products. We will spare no effort on providing you with technical assistance! Wish you success in your work!

## 1.2. Suggestion for Using IAC-IMX6-Kit Development Kit

1. Please read the instructions firstly, before using the single board computer;
2. Before using, please check the packing list and see whether there is a missing file in the CD;
3. Please understand the basic structure and composition of IAC-IMX6-Kit, including the hardware resource allocation etc.;
4. If you need to develop on Linux system and burn program into the development board, in addition to this document, we also suggest reading another document *IAC-IMX6-Kit Linux User Manual*;
5. IAC-IMX6-KIT accept batch order.

## II. System Composition

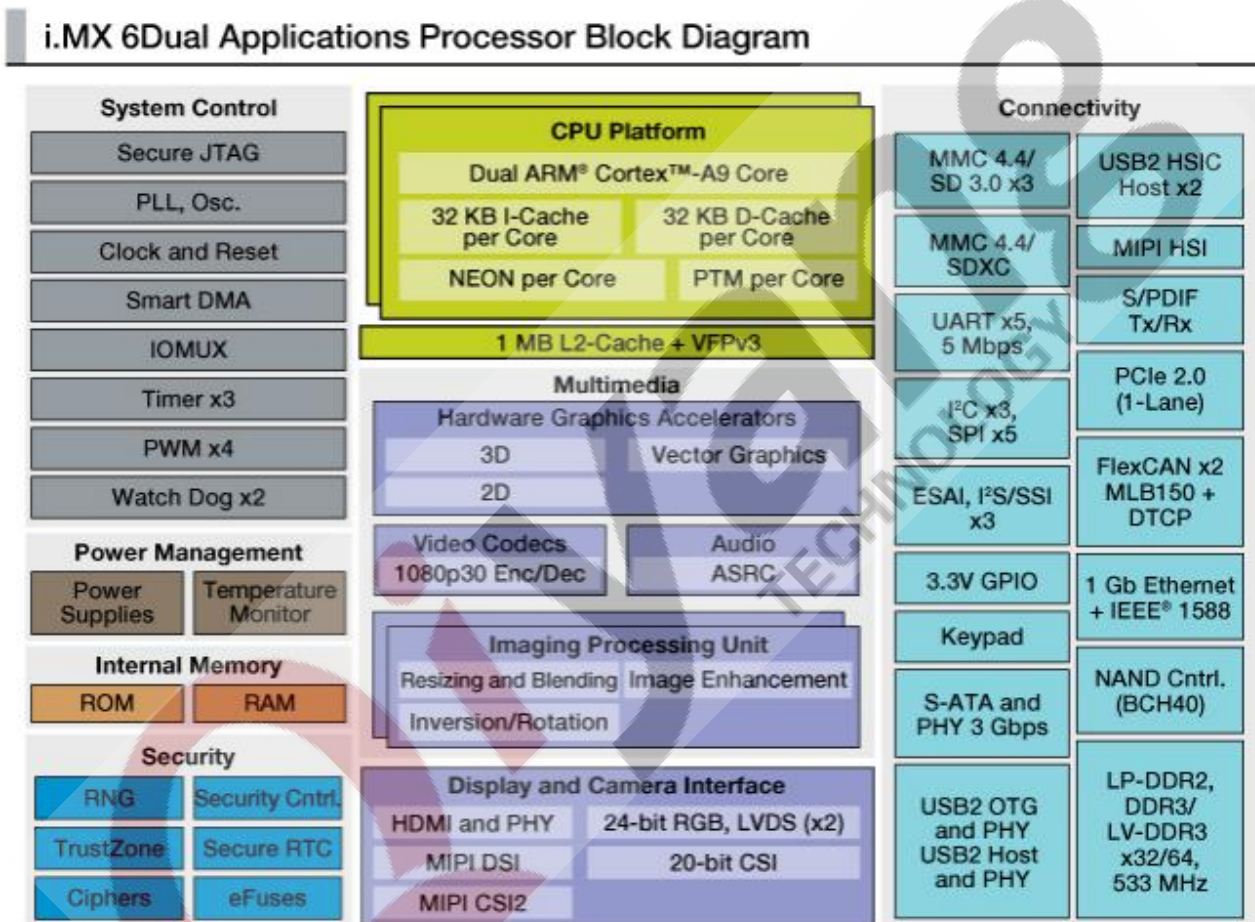
### 2.1 Chip Summary

IAC-IMX6-KIT embedded mainboard, adopts Freescale i.MX6 processor, Cortex-A9 core with 1.0 GHz and can be compatible with solo/dual /quad micro processor for upgrading. This mainboard is with standard i.MX6 Dual series:

The i.MX 6Dual family provides dual cores running up to 1.0 GHz with 1 MB of L2 cache and 64-bit DDR3 or 2-ch., 32-bit LPDDR2 support. Integrated FlexCAN, MLB bus, HD grade NEON SIMD Media Accelerator, Triple Play 3D/2D/VG Accelerator, 1080P video codec, PCI Express<sup>®</sup> and SATA-2 provide excellent conne

ctivity, while integration of LVDS, MIPI display port, MIPI camera port and HDMI v 1.4, the i.MX 6Dual provides a scalable solution for consumer, automotive and industrial applications.

Function diagram is as shown:



Picture 1

- ◆ ARM® Cortex™-A9, 1.0 GHz, compatible with solo/dual/quad core;
- ◆ 1MB L2 Cache, 32 KB instruction and data caches, NEON SIMD Media Accelerator;
- ◆ 2D/3D/VG Accelerator, 1080P h.264 video hardware codec, support dual 720P video encoding;
- ◆ 1x 20-bit parallel, MIPI-CSI2 (4-channel), three simultaneous inputs;
- ◆ 2-ch HOST USB HSIC, 1-ch OTG and 1-ch HOST USB integrated PHY;
- ◆ 1 industrial gigabit Ethernet MAC(10/100/1000MHZ);
- ◆ 2-ch CAN ports, each channel can up to 1 Mbps, support CAN2.0;
- ◆ 3 SD/MMC 4.4 and 1 SDXC;

Any question, please send E-mail : [supports@qiyangtech.com](mailto:supports@qiyangtech.com)

Sales E-mail : [trade@qiyangtech.com](mailto:trade@qiyangtech.com), [sales@qiyangtech.com](mailto:sales@qiyangtech.com)

Website: <http://www.qiytech.com>

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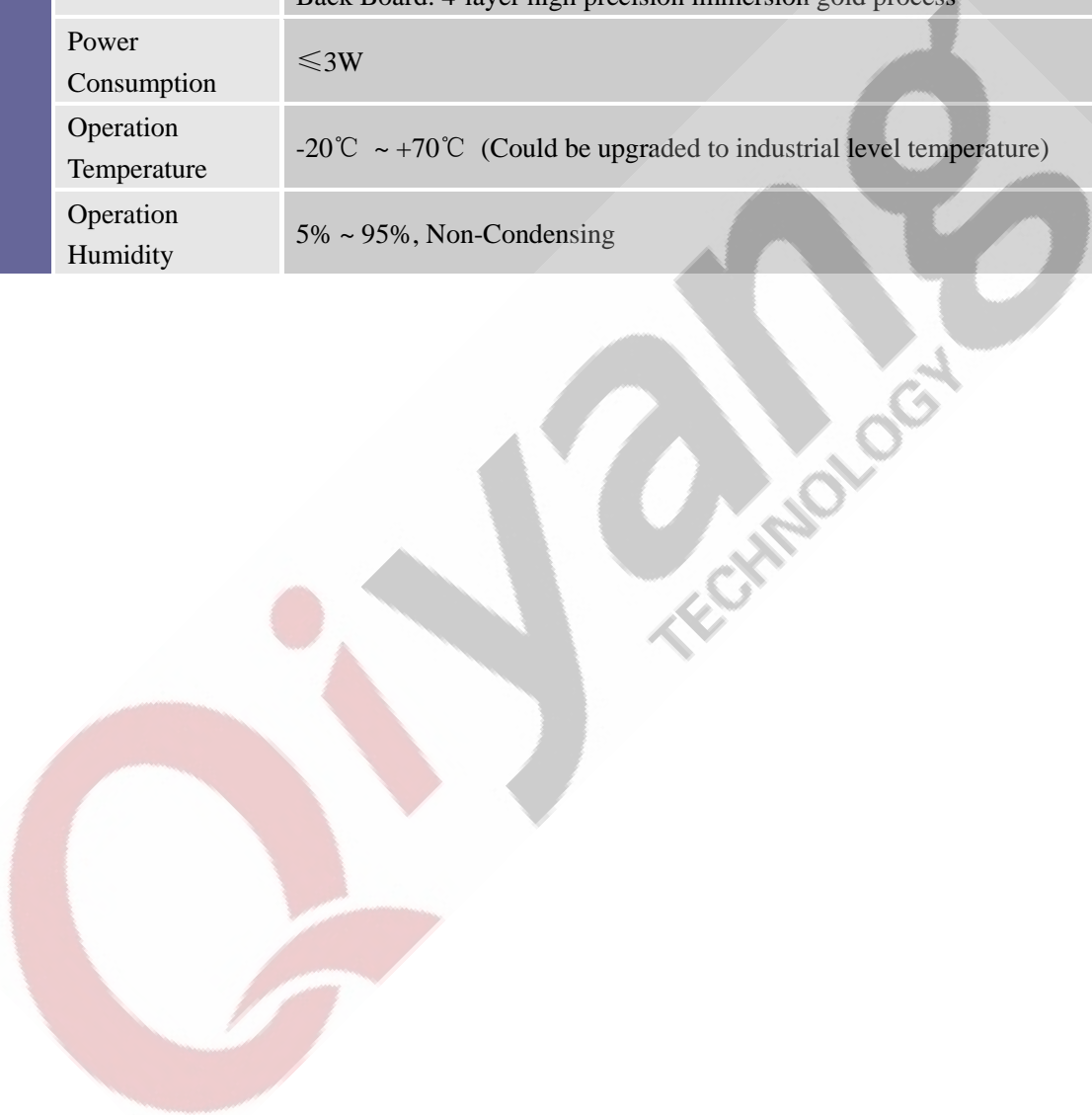
- ◆ 5 SPI, 5 UART, 3 I2C, 4 PWM;
- ◆ Integrated MIPI-HSI interface, 1-ch PCIe2.0 interface;
- ◆ Dual LVDS interface, support resolution up to 2048\*1536;
- ◆ Freescale PF100 PMU;
- ◆ High Assurance Boot, cryptographic cipher engines, random number generator, and tamper detection

## 2.2. Mainboard Resource

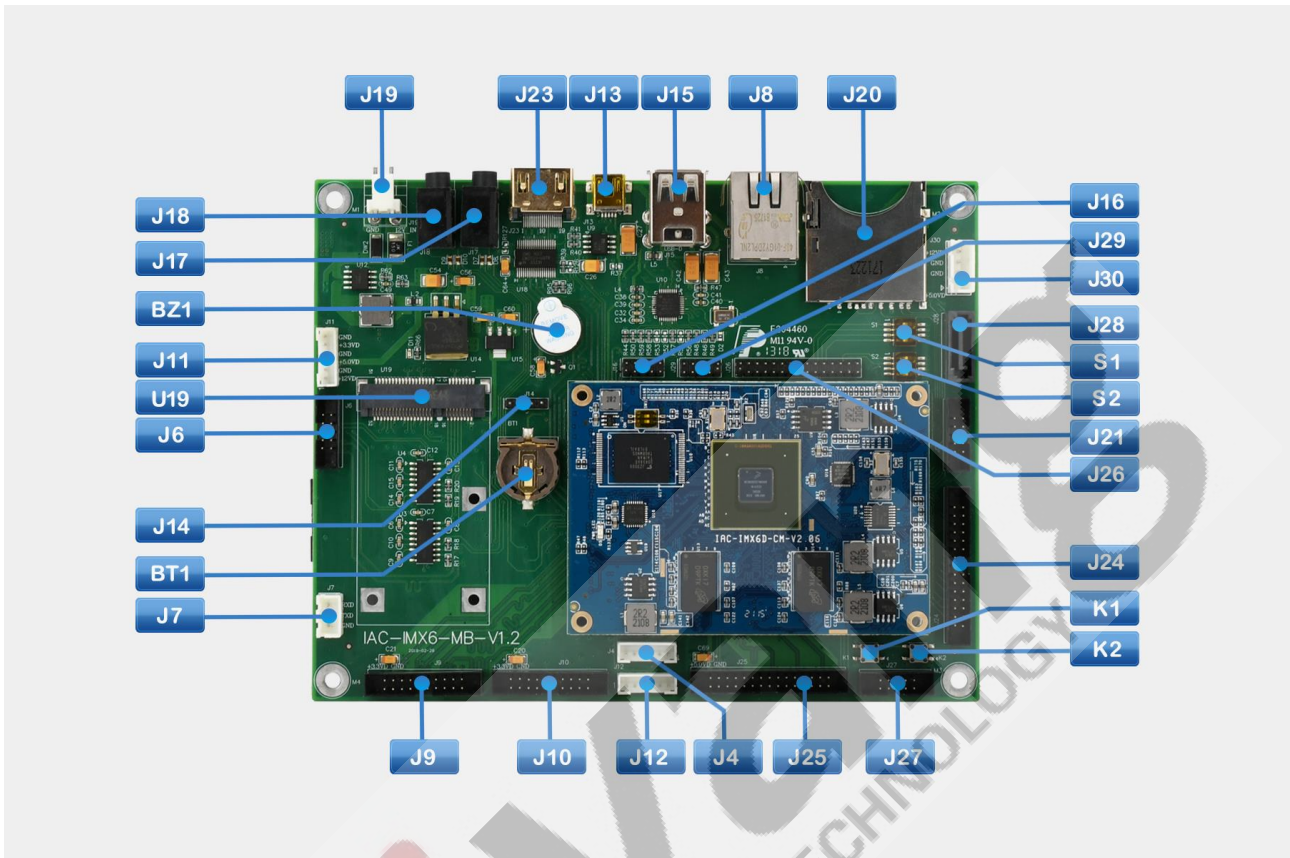
Hardware Resources	CPU	NXP i.MX6D processor, ARM® Cortex™-A9 core, 1.0 GHz, compatible with solo-core/dual-core/quad-core	
	RAM	DDR3 SDRAM, 4*256MB, total 1GB	
	Flash	4GB EMMC	
	Network	YT8511H Network Chip, adopt RGMII mode, support 10M/100M/1000M	
	Communication		3-ch RS232, 1 port as the debug UART, 2-ch RS232 multiplexes with RS485
			1-ch High speed USB OTG, 4-ch USB HOST, 1-ch connects to MIN_PCIE Interface
			2-ch CAN, support CAN2.0 protocol
			1-ch 10/100/1000Mbps Ethernet port, with ACT/LINK indicator
	Display		CSI&DSI interface
			2-ch LVDS port, each channel 's resolution up to 1920 * 1200
			HDMI, support HDMI 1.4
	Audio	McASP audio interface; binaural output; MIC audio input	
	Input	Standard I2C Capacitive panel	
	Expansion Interface		MINI_PCIE 2.0
		SIM Card	
		EIM Bus	
Memory Interface	1*SD Card Interface		
Other	1-ch SATA Hard disk interface, SATAII standard, speed up to 3.0Gbps		
Power Input	Reset circuit, Watch dog, RTC, Buzzer, JTAG interface		
Resource		+12V power supply, can support +4.75V~+18V wide range voltage supply	
	Development Tools		Development environment: Virtual Machine VM9.0.2+ubuntu12.04.1
			Application layer development debug tool
			Cross-compiler
		Common terminal development debugging tool	
Image File	OS image file, support multiple resolution display		



	Test Program	Interface using demo test program and test program source code
	Source Code	Bootloader, kernel, system source code
	Manual	Mainboard User Manual, Device Manual
	Mechanical Chart	Back Plane's Structure & Size
Electrical Specification	Size	Core Board:86mm*60mm
		Back Plane:160mm*125mm
	Layer	Core Board:8-layer high precision immersion gold process
		Back Board: 4-layer high precision immersion gold process
	Power Consumption	≤3W
	Operation Temperature	-20℃ ~ +70℃ (Could be upgraded to industrial level temperature)
	Operation Humidity	5% ~ 95%, Non-Condensing



### III. Interface Function



Picture 1

#### 3.1 Basic Interface Functional Description

Label #	Function
J4	CAN
J6	Serial Port 4/5 (Multiplex with RS485)
J7	Debug port
J8	Gigabit Ethernet
J9	LVDS (LVDS0)
J10	LVDS(LVDS1)

J11	+12V/+5.0V/+3.3V Reserved power supply interface
J12	Capacitive Touch Panel (I2C&GPIO )
J13	USB OTG
J14	USB port, could connect to the external module
J15	2-ch Standard USB
J16	2*4GPIO
J17	MIC audio input
J18	Audio output
J19	+12V power supply interface
J20	Standard SD Card
J21	SDIO
J23	HDMI
J24	EIM Bus
J25	CSI&DSI
J26	EIM
J27	2*4GPIO
J28	SATA
J29	MLB
J30	SATA Hard disk power interface
U19	MINI_PCIE

S1	Dial Switch
S2	Dial Switch
BT1	System clock power supply (+3.0V)
BZ1	Buzzer
K1	Reset button
K2	ON/OFF Signal button

### 3.2.Jumper Wire Dial-up Setting

Jumper Wire setting: SW1: BOOT MODE

Dial-up 1	Dial-up 2	BOOT MODE
0	0	BOOT FROM FUSE
0	1	SERIAL DOWNLOADER
1	0	INTERNAL BOOT
1	1	RESERVED

Interface Board's dial-up setting: S1 & S2 select BOOT DEVICE

BOOT DEVICE	S1				S2			
	1	2	3	4	1	2	3	4
SD	X	0	1	0	0	1	0	1
EMMC	X	1	1	0	1	1	0	1

### 3.3. Pin Definition

J1:

Signal Name	PIN#	PIN#	Signal Name
USB_H1_VBUS	1	2	USB_OTG_VBUS
USB_H1_DP	3	4	USB_OTG_DN
USB_H1_DN	5	6	USB_OTG_DP
GPIO_1	7	8	USB_OTG_ID
GPIO_2	9	10	GND
GPIO5_IO19	11	12	PCIE_CLK_N
GPIO5_IO21	13	14	PCIE_CLK_P
GPIO5_IO18	15	16	GND
GPIO5_IO20	17	18	PCIE_RXM
I2C1_SCL	19	20	PCIE_RXP
I2C1_SDA	21	22	GND
DBG_TXD	23	24	PCIE_TXM
DBG_RXD	25	26	PCIE_TXP
GPIO5_IO30	27	28	GND
GPIO5_IO31	29	30	HOTPLUG_DET
GPIO6_IO00	31	32	HDMI_CEC_IN
GPIO6_IO01	33	34	GND
GPIO6_IO02	35	36	HDMI_CLKM
GPIO6_IO03	37	38	HDMI_CLKP
GPIO6_IO04	39	40	GND
GPIO6_IO05	41	42	HDMI_D0M
GND	43	44	HDMI_D0P

MIC_INP	45	46	GND
MIC_INM	47	48	HDMI_D1M
LINE_INL	49	50	HDMI_D1P
LINE_INR	51	52	GND
HPLOUT	53	54	HDMI_D2M
HPROUT	55	56	HDMI_D2P
GND	57	58	GND
UART5_TXD	59	60	UART4_TXD
UART5_RXD	61	62	UART4_RXD
CAN2_TXD	63	64	CAN1_TXD
CAN2_RXD	65	66	CAN1_RXD
I2C3_SCL	67	68	I2C2_SCL
I2C3_SDA	69	70	I2C2_SDA
GND	71	72	GND
LVDS1_TXN0	73	74	LVDS0_TXN0
LVDS1_TXP0	75	76	LVDS0_TXP0
GND	77	78	GND
LVDS1_TXN1	79	80	LVDS0_TXN1
LVDS1_TXP1	81	82	LVDS0_TXP1
GND	83	84	GND
LVDS1_TXN2	85	86	LVDS0_TXN2
LVDS1_TXP2	87	88	LVDS0_TXP2
GND	89	90	GND
LVDS1_CLKN	91	92	LVDS0_CLKN
LVDS1_CLKP	93	94	LVDS0_CLKP
GND	95	96	GND

LVDS1_TXN3	97	98	LVDS0_TXN3
LVDS1_TXP3	99	100	LVDS0_TXP3

J2:

Signal Name	PIN#	PIN#	Signal Name
GND	1	2	GND
GMDN1	3	4	GMDP2
GMDP1	5	6	GMDN2
GND	7	8	GND
GMDN0	9	10	GMDP3
GMDP0	11	12	GMDN3
LED_LINK	13	14	LED_ACT
GND	15	16	GND
SATA_TXP	17	18	SD2_CLK
SATA_TXM	19	20	SD2_CMD
GND	21	22	SD2_DAT0
SATA_RXP	23	24	SD2_DAT1
SATA_RXM	25	26	SD2_DAT2
GND	27	28	SD2_DAT3
SD3_CLK	29	30	SD2_CMD
SD3_DAT0	31	32	SD2_DAT4
SD3_DAT1	33	34	SD2_DAT5
SD3_DAT2	35	36	SD2_DAT6
SD3_DAT3	37	38	SD2_DAT7
EIM_OEN	39	40	SYS_RSTN
EIM_WRN	41	42	EIM_LBA

EIM_CS1N	43	44	EIM_CS0N
EIM_D17	45	46	EIM_D16
EIM_D19	47	48	EIM_D18
EIM_D21	49	50	EIM_D20
EIM_D23	51	52	EIM_D22
EIM_D25	53	54	EIM_D24
EIM_D27	55	56	EIM_D26
EIM_D29	57	58	EIM_D28
EIM_D31	59	60	EIM_D30
EIM_WAIT	61	62	EIM_BCLK
EIM_AD14	63	64	EIM_AD13
GPIO4_IO16	65	66	EIM_AD15
GPIO4_IO19	67	68	GPIO4_IO18
GPIO4_IO17	69	70	GPIO4_IO20
GPIO4_IO22	71	72	GPIO4_IO21
GPIO4_IO24	73	74	GPIO4_IO23
GPIO4_IO26	75	76	GPIO4_IO25
GPIO4_IO28	77	78	GPIO4_IO27
GPIO4_IO30	79	80	GPIO4_IO29
GPIO5_IO05	81	82	GPIO4_IO31
GPIO5_IO07	83	84	GPIO5_IO06
GPIO5_IO09	85	86	GPIO5_IO08
GPIO5_IO11	87	88	GPIO5_IO10
TSC_YP	89	90	TSC_XP
TSC_YM	91	92	TSC_XM
+5.0VD	93	94	+5.0VD



+5.0VD	95	96	+5.0VD
+5.0VD	97	98	+5.0VD
+5.0VD	99	100	+5.0VD

J3:

Signal Name	PIN#	PIN#	Signal Name
GND	1	2	GND
DSI_CLK0M	3	4	JTAG_TCK
DSI_CLK0P	5	6	JTAG_TMS
GND	7	8	JTAG_TDI
DSI_D0M	9	10	JTAG_TDO
DSI_D0P	11	12	JTAG_TRSTN
GND	13	14	GND
DSI_D1M	15	16	NC
DSI_D1P	17	18	GND
GND	19	20	GPIO_4
CSI_D0M	21	22	GPIO_7
CSI_D0P	23	24	GPIO_8
GND	25	26	GPIO_9
CSI_D1M	27	28	GND
CSI_D1P	29	30	GPIO_16
GND	31	32	GPIO_17
CSI_D2M	33	34	GPIO_18
CSI_D2P	35	36	GPIO_19
GND	37	38	GND
CSI_D3M	39	40	NC

CSI_D3P	41	42	NC
GND	43	44	ON/OFF
CSI_CLK0M	45	46	NC
CSI_CLK0P	47	48	NC
GND	49	50	NC
GND	51	52	NC
MLB_SP	53	54	NC
MLB_SN	55	56	NC
GND	57	58	NC
MLB_DP	59	60	NC
MLB_DN	61	62	NC
GND	63	64	NC
MLB_CP	65	66	NC
MLB_CN	67	68	NC
GND	69	70	NC
NC	71	72	NC
GND	73	74	GND
CSII_D13	75	76	CSII_PCLK
CSII_D15	77	78	CSII_D12
CSII_D17	79	80	CSII_D14
CSII_D19	81	82	CSII_D16
CSII_D11	83	84	CSII_D18
CSII_D9	85	86	CSII_D10
CSII_D7	87	88	CSII_D8
CSII_D5	89	90	CSII_D6
CSII_D3	91	92	CSII_D4

CSI1_D1	93	94	CSI1_D2
CSI1_DEN	95	96	CSI1_D0
CSI1_VS	97	98	CSI1_HS
GND	99	100	GND

**J4:CAN**

PIN#	Signal Name
1	CANH1
2	CANL1
3	CANH2
4	CANL2
5	GND_CAN
6	GND_CAN

**J6:Serial Port 4/5**

Signal Name	PIN#	PIN#	Signal Name
J_RXD4	1	2	J_TXD4
J_RXD5	3	4	J_TXD5
GND	5	6	GND
J_485A4	7	8	J_485B4
J_485A5	9	10	J_485B5

**J7:Debug Port**

PIN#	Signal Name
1	J_DRXD
2	J_DTXD
3	GND

## J9:LVDS (LVDS0)

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	+3.3VD
GND	3	4	GND
LVDS0_TXN0	5	6	LVDS0_TXP0
GND	7	8	GND
LVDS0_TXN1	9	10	LVDS0_TXP1
LVDS0_TXN2	11	12	LVDS0_TXP2
GND	13	14	GND
LVDS0_CLKN	15	16	LVDS0_CLKP
LVDS0_TXN3	17	18	LVDS0_TXP3
GPIO4_IO18	19	20	GPIO4_IO20

## J10:LVDS (LVDS1)

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	+3.3VD
GND	3	4	GND
LVDS1_TXN0	5	6	LVDS1_TXP0
GND	7	8	GND
LVDS1_TXN1	9	10	LVDS1_TXP1
LVDS1_TXN2	11	12	LVDS1_TXP2
GND	13	14	GND
LVDS1_CLKN	15	16	LVDS1_CLKP
LVDS1_TXN3	17	18	LVDS1_TXP3
GPIO4_IO21	19	20	GPIO4_IO23

## J11:Reserved Power Supply Interface

Any question, please send E-mail :[supports@qiyangtech.com](mailto:supports@qiyangtech.com)

Sales E-mail :[trade@qiyangtech.com](mailto:trade@qiyangtech.com),[sales@qiyangtech.com](mailto:sales@qiyangtech.com)

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PIN#	Signal Name
1	+12.0VD
2	GND
3	+5.0VD
4	GND
5	+3.3VD
6	GND

**J12: Capacitive Touch Panel Interface**

PIN#	Signal Name
1	+3.3VD
2	GND
3	I2C3_SDA
4	I2C3_SCL
5	GPIO4_IO25
6	GPIO4_IO27

**J14:USB**

PIN#	Signal Name
1	GND
2	USBDN_DM4
3	USBDN_DP4
4	+3.3VD

**J16:2\*4GPIO**

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	GND
GPIO_4	3	4	GPIO_7

GPIO_8	5	6	GPIO_9
GPIO_16	7	8	GPIO_17
GPIO_18	9	10	GPIO_19

**J21:SDIO**

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	GND
SYS_RSTN	3	4	SD2_DAT0
SD2_DAT3	5	6	SD2_CMD
SD2_DAT2	7	8	SD2_CLK
SD2_DAT1	9	10	NC

**J23:HDMI**

PIN#	Signal Name
1	HDMS_D2P
2	GND
3	HDMS_D2M
4	HDMS_D1P
5	GND
6	HDMS_D1M
7	HDMS_D0P
8	GND
9	HDMS_D0M
10	HDMS_CLKP
11	GND
12	HDMS_CLKM
13	HDMS_CEC_OUT

14	NC
15	DDC_CLK
16	DDC_DAT
17	GND
18	HDMI_5V
19	HP_DET

**J24:EIM**

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	+3.3VD
EIM_AD15	3	4	EIM_AD14
EIM_AD13	5	6	EIM_WAIT
EIM_BCLK	7	8	EIM_D31
EIM_D30	9	10	EIM_D29
EIM_D28	11	12	EIM_D27
EIM_D26	13	14	EIM_D25
EIM_D24	15	16	EIM_D23
EIM_D22	17	18	EIM_D21
EIM_D20	19	20	EIM_D19
EIM_D18	21	22	EIM_D17
EIM_D16	23	24	EIM_CS1N
EIM_CS0N	25	26	EIM_WRN
EIM_LBA	27	28	EIM_OEN
GND	29	30	GND

**J25:CSI&DSI**

Signal Name	PIN#	PIN#	Signal Name
-------------	------	------	-------------

CSI_D0M	1	2	+5.0VD
CSI_D0P	3	4	+5.0VD
GND	5	6	I2C2_SDA
CSI_D1M	7	8	I2C2_SCL
CSI_D1P	9	10	GPIO4_IO16
GND	11	12	GPIO4_IO19
CSI_D2M	13	14	GPIO4_IO17
CSI_D2P	15	16	DSI_D0M
GND	17	18	DSI_D0P
CSI_D3M	19	20	GND
CSI_D3P	21	22	DSI_CLK0M
GND	23	24	DSI_CLK0P
CSI_CLK0M	25	26	GND
CSI_CLK0P	27	28	DSI_D1M
GND	29	30	DSI_D1P

## J26:EIM

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	+3.3VD
+3.3VD	3	4	+3.3VD
CSII_PCLK	5	6	CSII_D13
CSII_D12	7	8	CSII_D15
CSII_D14	9	10	CSII_D17
CSII_D16	11	12	CSII_D19
CSII_D18	13	14	CSII_D11
CSII_D10	15	16	CSII_D9



CSI1_D8	17	18	CSI1_D7
CSI1_D6	19	20	CSI1_D5
CSI1_D4	21	22	CSI1_D3
CSI1_D2	23	24	CSI1_D1
CSI1_D0	25	26	CSI1_DEN
CSI1_HS	27	28	CSI1_VS
GND	29	30	GND

**J27:2\*4GPIO**

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	GND
GPIO4_IO24	3	4	GPIO4_IO26
GPIO4_IO28	5	6	GPIO4_IO30
GPIO5_IO05	7	8	GPIO5_IO07
GPIO5_IO09	9	10	GPIO5_IO11

**J28:SATA**

PIN#	Signal Name
1	GND
2	SATA_TXP
3	SATA_TXM
4	GND
5	SATA_RXM
6	SATA_RXP
7	GND
8	GND_SATA
9	GND_SATA

## J29:MLB

Signal Name	PIN#	PIN#	Signal Name
+3.3VD	1	2	+3.3VD
MLB_SP	3	4	MLB_SN
MLB_DP	5	6	MLB_DN
MLB_CP	7	8	MLB_CN
GND	9	10	GND

## J30:SATA Power Supply Interface

PIN#	Signal Name
1	+5.0VD
2	GND
3	GND
4	+12.0VD

## U19:MINI\_PCIE

Signal Name	PIN#	PIN#	Signal Name
Reserved	1	2	PCIE_3V3
Reserved	3	4	GND
Reserved	5	6	PCIE_1V5
Reserved	7	8	UIM_PWR
GND	9	10	UIM_DATA
PCIE_CLK1_N	11	12	UIM_CLK
PCIE_CLK1_P	13	14	UIM_RESET
GND	15	16	UIM_VPP
Reserved	17	18	GND

Reserved	19	20	GPIO5_IO19
GND	21	22	GPIO5_IO21
PCIE_RXM	23	24	PCIE_3V3
PCIE_RXP	25	26	GND
GND	27	28	PCIE_1V5
GND	29	30	I2C2_SCL
PCIE_TXM	31	32	I2C2_SDA
PCIE_TXP	33	34	GND
Reserved	35	36	USBDN_DM3
Reserved	37	38	USBDN_DP3
Reserved	39	40	GND
Reserved	41	42	LED_WWAN
Reserved	43	44	LED_WLAN
Reserved	45	46	LED_WPAN
Reserved	47	48	PCIE_1V5
Reserved	49	50	GND
Reserved	51	52	PCIE_3V3

## U20:SIM Card Interface

Signal Name	PIN#	PIN#	Signal Name
UIM_PWR	1	4	GND
UIM_RESET	2	5	UIM_VPP
UIM_CLK	3	6	UIM_DATA

## IV. Software Description

IAC-IMX6-KIT provides the software support for Linux & Android.

The *IAC-IMX6-Kit Linux User Manual* will introduce the IAC-IMX6-Kit development kit's setting up and using in Linux . The detailed content could refer to the relative documentation.

## VI. Remark

1. Before connect to LCD, confirm LCD power specification.
2. Please use the original connecting accessories, avoid damaging the main board.
3. We ensure offering communication technology support through E-mail, telephone for lifelong technical support service.
4. We ensure offering 6 months repair service for free, if malfunction occurs in warranty because of quality problem, contact our retailer or our company with purchase receipt in warranty period, we will repair or replace it.
5. Under these circumstances, we do not offer repair for free:
  - Over warranty time;
  - Do not have purchase receipt;
  - Liquid inlet, Damp or Mold;
  - Malfunction and damage is not due to product quality but drops, intense sharking, arbitrarily modify, disoperation after purchase;
  - Damage of force majeure.
6. We reserve intellectual property for the software and hardware technical data of QY-IMX6S; users can only use them for teaching, testing, researching. Shall not be

engaged in any commercial purpose. Shall not distribute them on the Internet. Shall not intercept, modify them to tamper copyright.

7. We accept batch order; we can offer technical support and service.



## Zhejiang Qiyang Intelligent Technology Co., Ltd

Tel: 86-571-87858811 / 87858822

Fax: 86-571-89935912

Technology Support: 86-571-89935913

E-MAIL: [supports@qiyangtech.com](mailto:supports@qiyangtech.com)

Website: <http://www.qiytech.com>

Address: 3RD floor, Building A, WSCG Building, NO.6 Xiyuan 8th  
Road, Sandun Town, Xihu District, Hangzhou City,  
Zhejiang ,PRC .310030 Post Code: 310030